

User Manual

CodeVisionAVR V1.0.2.1 User Manual Revision N Copyright © 1998-2001 Pavel Haiduc and HP InfoTech S.R.L. All rights reserved. No part of this document may be reproduced in any form except by written permission of the author. All rights of translation reserved.

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1. Introduction

CodeVisionAVR is a C cross-compiler, Integrated Development Environment and Automatic Program Generator designed for the Atmel AVR family of microcontrollers.

The program is a native 32bit application that runs under the Windows 95, 98, NT 4.0 and 2000 operating systems.

The C cross-compiler implements nearly all the elements of the ANSI C language, as allowed by the AVR architecture, with some features added to take advantage of specificity of the AVR architecture and the embedded system needs.

The compiled COFF object files can be C source level debugged, with variable watching, using the Atmel AVR Studio debugger version 3.50 or later.

The Integrated Development Environment (IDE) has built-in AVR Chip In-System Programmer software that enables the automatical transfer of the program to the microcontroller chip after successful compilation/assembly. The In-System Programmer software is designed to work in conjunction with the Atmel STK500, Kanda Systems STK200+/300, Dontronics DT006, Vogel Elektronik VTEC-ISP and MicroTronics' ATCPU, Mega2000 development boards.

For debugging embedded systems, which employ serial communication, the IDE has a built-in Terminal.

Besides the standard C libraries, the CodeVisionAVR C compiler has dedicated libraries for:

- Alphanumeric LCD modules
- Philips I²C bus
- National Semiconductor LM75 Temperature Sensor
- Philips PCF8563, PCF8583, Dallas Semiconductor DS1302 and DS1307 Real Time Clocks
- Dallas Semiconductor 1 Wire protocol
- Dallas Semiconductor DS1820/DS1822 Temperature Sensors
- Dallas Semiconductor DS1621 Thermometer/Thermostat
- Dallas Semiconductor DS2430 and DS2433 EEPROMs
- SPI
- Power management
- Delays
- Gray code conversion.

CodeVisionAVR also contains the CodeWizardAVR Automatic Program Generator, that allows you to write, in a matter of minutes, all the code needed for implementing the following functions:

- External memory access setup
- Chip reset source identification
- Input/Output Port initialization
- External Interrupts initialization
- Timers/Counters initialization
- Watchdog Timer initialization
- UART initialization and interrupt driven buffered serial communication
- Analog Comparator initialization
- ADC initialization
- SPI Interface initialization

• I²C Bus, LM75 Temperature Sensor, DS1621 Thermometer/Thermostat and PCF8563, PCF8583, DS1302, DS1307 Real Time Clocks initialization

- 1 Wire Bus and DS1820/DS1822 Temperature Sensors initialization
- LCD module initialization.

This product is © Copyright 1998-2001 Pavel Haiduc and HP InfoTech S.R.L., all rights reserved. The author of the program wishes to thank Mr. Jack Tidwell for his great help in the implementation of floating point routines and to Mr. Yuri G. Salov for his excellent work in improving the Mathematical Functions Library and beta testing CodeVisionAVR.

2. CodeVisionAVR Integrated Development Environment

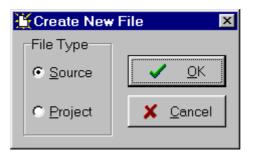
2.1 Working with Files

Using the CodeVisionAVR IDE you can view and edit any text file used or produced by the C compiler or assembler.

2.1.1 Creating a New File

You can create a new source file using the **File|New** menu command or by pressing the **Create new file** button on the toolbar.

A dialog box appears, in which you must select File Type|Source and press the Ok button.



A new editor window appears for the newly created file.

The new file has the name **untitled.c**. You can save this file under a new name using the **File|Save As** menu command.

2.1.2 Opening an Existing File

You can open an existing file using the **File|Open** menu command or by pressing the **Open file** button on the toolbar.

An **Open** dialog window appears.

Open		?	×
Look <u>i</u> n:	😋 Ds1820	- 🖻 🗹 💣 🔳	
🔳 Ds1820			1
			4
File <u>n</u> ame:	Ds1820	<u>O</u> pen	
Files of typ	e: C Compiler files (*.c;*.h)		1
~.	, , , , ,		

You must select the name and type of file you wish to open.

By pressing the **Open** button you will open the file in a new editor window.

2.1.3 Files History

The CodeVisionAVR IDE keeps a history of the opened files. The most recent eight files that where used can be reopened using the **File**|**Reopen** menu command.

2.1.4 Editing a File

A previously opened or a newly created file can be edited in the editor window by using the **Tab**, **Arrows**, **Backspace** and **Delete** keys.

Pressing the **Home** key moves the cursor to the start of the current text line.

Pressing the End key moves the cursor to the end of the current text line.

Pressing the Ctrl+Home keys moves the cursor to the start of the file.

Pressing the **Ctrl+End** keys moves the cursor to the end of the file.

Portions of text can be selected by dragging with the mouse.

You can copy the selected text to the clipboard by using the **Edit|Copy** menu command, by pressing the **Ctrl+C** keys or by pressing the **Copy** button on the toolbar.

By using the **Édit|Cut** menu command, by pressing the **Ctrl+X** keys or by pressing the **Cut** button on the toolbar, you can copy the selected text to the clipboard and then delete it from the file. Text previously saved in the clipboard can be placed at the current cursor position by using the

Edit|Paste menu command, by pressing the Ctrl+V keys or pressing the Paste button on the toolbar.

Clicking in the right margin of the editor window allows selection of a whole line of text. Selected text can be deleted using the **Edit|Delete** menu command or pressing the **Ctrl+Delete** keys. The **Edit|Print Selection** menu command allows the printing of the selected text. Dragging and dropping with the mouse can move portions of text.

Pressing the Ctrl+Y keys deletes the text line where the caret is currently positioned.

Selected portions of text can be indented, respectively unindented, using the **Edit|Indent Block**, respectively **Edit|Unindent Block**, menu commands or by pressing the **Ctrl+I**, respectively **Ctrl+U** keys.

You can find, respectively replace, portions of text in the edited file by using the **Edit|Find**, respectively **Edit|Replace**, menu commands, by pressing the **Ctrl+F**, respectively **Ctrl+R** keys, or by pressing the **Find**, respectively **Replace** buttons on the toolbar.

Changes in the edited text can be undone, respectively redone, by using the **Edit|Undo**, respectively **Edit|Redo**, menu commands, by pressing the **Ctrl+Z**, respectively **Shift+Ctrl+Z** keys, or by pressing the **Undo**, respectively **Redo** buttons on the toolbar.

You can go to a specific line number in the edited file, by using the **Edit|Goto Line** menu command or by pressing the **Alt+G** keys.

Bookmarks can be inserted or removed, at the line where the cursor is positioned, by using the **Edit|Toggle Bookmark** menu command or by pressing the **Shift+Ctrl+0...9** keys. The **Edit|Jump to Bookmark** menu command or the **Ctrl+0...9** keys will position the cursor at the start of the corresponding bookmarked text line.

If the cursor is positioned on an opening, respectively closing, brace then the **Edit|Match Braces** menu command or the **Ctrl+M** key will highlight, the portion of text until the corresponding matching closing, respectively opening brace. Pressing any key or clicking the mouse will hide the highlighting.

Clicking with the mouse right button opens a pop-up menu that also gives the user access to the above mentioned functions.

2.1.5 Saving a File

The currently edited file can be saved by using the **File|Save** menu command, by pressing the **Ctrl+S** keys or by pressing the **Save** button on the toolbar.

When saving, the Editor will create a backup file with an ~ character appended to the extension.

All currently opened files can be saved using the File|Save All menu command.

2.1.6 Renaming a File

The currently edited file can be saved under a new name by using the **File|Save As** menu command. A **Save** dialog window will open.

Save C:\C	VAV	\BIN\untitled.c	As				? ×
Save <u>i</u> n:	🔁 B	1		- 🗈 🜌	ä		
							- 1
							- 1
							- 1
							- 1
I							_
File <u>n</u> ame:		est				<u>S</u> ave	
Save as ty	/pe:	C Compiler source	file (*.c)		-	Cancel	

You will have the possibility to specify the new name and type of the file, and eventually its new location.

2.1.7 Printing a File

You can print the current file using the **File**|**Print** menu command or by pressing the **Print** button on the toolbar.

The contents of the file will be printed to the Windows default printer.

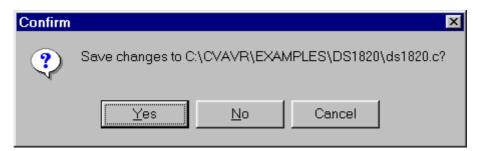
The paper margins used when printing can be set using the **File|Page Setup** menu command, which opens the **Page Setup** dialog window.

🛿 Page Setup	×
Printing Options	Margins
✓ Page <u>H</u> eader	Left: 30 🏹 Right: 19 🏹
✓ Page Numbers	Top: 19 🏒 Bottom: 19 🏒
☑ Highlight Syntax	Units: mm 💌
🗃 <u>P</u> rinter	V QK X Cancel

The units used when setting the paper margins are specified using the **Units** list box. The printer can be configured by pressing the **Printer** button in this dialog window. Changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

2.1.8 Closing a File

You can quit editing the current file by using the **File**|**Close** menu command. If the file was modified, and wasn't saved yet, you will be prompted if you want to do that.

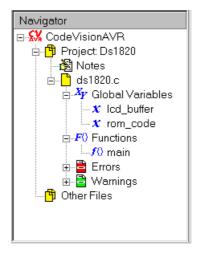


Pressing **Yes** will save changes and close the file. Pressing **No** will close the file without saving the changes. Pressing **Cancel** will disable the file closing process.

All currently opened files can be closed using the File|Close All menu command.

2.1.9 Using the Navigator

The **Navigator** window allows easy displaying or opening of source files. By clicking on the file name the appropriate file is maximized or opened.



After a **Compile** or **Make** process there is also displayed a list of global variables and functions declared in each compiled C source file.

By clicking on the variable's, respective function's, name the variable, respective function, declaration is highlighted in the appropriate C source file.

If during compilation there are errors or warnings, these are also displayed in the **Navigator** window. By clicking on the error or warning, the corresponding source line is highlighted in the appropriate file.

The Navigator tree branches can be expanded, respectively collapsed, by clicking on the +, respectively -, buttons.

By right clicking in the Navigator window you can open a pop-up menu with the following choices:

- Open a file
- Save the currently edited file
- Save All opened files
- Close Current File
- Close Project
- Close All opened files
- Toggle on or off expanding the file branches
- Toggle on or off expanding the **Errors** and **Warnings** branches for the file whose Editor window has focus

2.2 Working with Projects

The Project groups the source file(s) and compiler settings that you use for building a particular program.

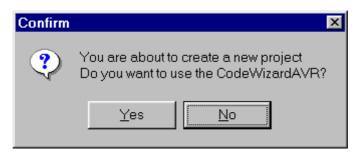
2.2.1 Creating a New Project

You can create a new Project using the **File|New** menu command or by pressing the **Create new file** button on the toolbar.

A dialog box appears, in which you must select File Type|Project and press the OK button.

🗳 Create New	File 🗵
File Type	
C <u>S</u> ource	✓ <u>о</u> к
● <u>P</u> roject	X <u>C</u> ancel

A dialog will open asking you to confirm if you would like to use the CodeWizardAVR to create the new project.



If you select No then the Create New Project dialog window will open.

You must specify the new Project file name and its location.

Create New Pr	oject		? ×
Save in: 🔂 B	Examples	• 🖻 🗹 🖸	* 🔳
C_asm Ds1820 Eeprom CKeypad Lcddemo Led Max1241 Spi	Therm75		
File <u>n</u> ame:	test		<u>S</u> ave
Save as <u>t</u> ype:	Project files (*.prj)	•	Cancel

The Project file will have the .prj extension. You can configure the Project by using the **Project|Configure** menu command.

2.2.2 Opening an Existing Project

You can open an existing Project file using the **File|Open** menu command or by pressing the **Open file** button on the toolbar.

An $\ensuremath{\textbf{Open}}$ dialog window appears.

Open											? ×
Look <u>i</u> n:	🔁 Mi	ultfile			 	- 🗈	<u></u>	d *			
🔊 Multfile											
File <u>n</u> ame:	: [Multfile						-		<u>)</u> pen	
Files of typ	be: [Project f	iles (*.p	rj)			<u>-</u>]	С	ancel	

You must select the file name of the Project you wish to open.

By pressing the **Open** button you will open the Project file and its source file(s).

You can configure the Project by using the Project|Configure menu command.

2.2.3 Adding Notes or Comments to the Project

With every Project the CodeVisionAVR IDE creates a text file where you can place notes and comments.

You can access this file using the **Project|Notes** or **Windows** menu commands.

🖓 Project Notes - Multfile.prj	_ 🗆 🗵

This file can be edited using the standard Editor commands.

The file is automatically saved when you **Close** the Project or **Quit** the CodeVisionAVR program.

2.2.4 Configuring the Project

The Project can be configured using the **Project|Configure** menu command or the **Project Configure** toolbar button.

2.2.4.1 Adding or removing a File from the Project

To add or remove a file from the currently opened project you must use the **Project|Configure** menu command.

A Configure Project tabbed dialog window will open. You must select the Files tab.

💦 Config	gure Project Multfile.prj			x
Files	C Compiler After Make			1
	C:\CVAVR\Examples\MULTFILE\Multfile.prj mainfile.c file1.c file2.c file3.c		<mark>. Add</mark> ∰ <u>R</u> emove	고 있다. 2014년 - 2014년 -
		ancel	? <u>H</u> elp	

By pressing the Add button you can add a source file to the project.

The first file added to the project is the main project file.

This file will always be Maked.

The rest of the files added to the project will be automatically linked to the main project file on Make.

Multiple files can be added by holding the Ctrl key when selecting in the Add File to Project dialog.

Add File To	Project		? ×
Lookin: 🧲	🖻 Multfile	- 🗈 🜌	
file1 file2 file3 a mainfile			
File <u>n</u> ame:	"file3.c" "file2.c" "file1.c"		<u>O</u> pen
Files of type:	C Compiler files (*.c)	•	Cancel

When the project is **Open**-ed all project files will be opened in the editor. By clicking on a file, and then pressing the **Remove** button, you will remove this file from the project.

Changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

2.2.4.2 Setting the C Compiler Options

To set the C compiler options for the currently opened project you must use the **Project|Configure** menu command.

A Configure Project tabbed dialog window will open. You must select the C Compiler tab.

💦 Configure Project Multfile.p	orj		X
Files C Compiler After Mal	<e td="" <=""><td></td><td></td></e>		
Chip: ATmega163 Clock: 4.000000 UART Initialize Baud Rate Baud: 9600 Memory Model © Tiny © Small Optimize for © Speed	SRAM Data Stack size: Internal SRAM size: External SRAM size: External SRAM wait Compilation Bit Variables size: 16 Promote char to int Ø Global #define Ø Automatic Register A Duse an External Star Ø Enable Warnings Stack End Markers	 ✓ char is <u>un</u> ✓ Enhanced Allocation tup Initialization 	d Instr. on File
Program Type: Application	File Output Format(s):		
		1	1
	<u>✓ o</u> k <u>× c</u> e	ancel	? <u>H</u> elp

You can select the target AVR microcontroller chip by using the **Chip** combo box.

You must also specify the **CPU Clock Frequency** in MHz, which is needed by the Delay Functions, 1 Wire Protocol Functions, Dallas Semiconductor DS1820/DS1822 Temperature Sensors Functions and for the UART initialization.

If the program uses serial communication, you must check the **Initialize UART** check box and specify the **Baud** rate. The compiler will automatically generate start-up code to initialize the UART's Baud rate.

The required memory model can be selected by using the **Memory Model** radio group box.

The compiled program can be optimized for minimum size, respectively maximum execution speed, using the **Optimize for|Size**, respectively **Optimize for|Speed**, settings.

For devices that allow self-programming the Program Type can be selected as:

- Application
- Boot Loader

The Data Stack Size must be also specified.

Eventually you may also specify the **External SRAM Size** (in case the microcontroller have external SRAM memory connected).

The **External SRAM Wait State** option enables the insertion of wait states during access to the external SRAM. This is useful when using slow memory devices.

If an Atmel AT94K05, AT94K10, AT94K20 or AT94K40 FPSLIC device will be used, than there will be the possibility to specify the **Program SRAM size** in Kwords.

👫 Configure Project Multfile.p	prj 🛛	×
Files C Compiler After Mal	ke	
Chip: AT94K Clock: 4.000000 MHz UART Initialize Baud Rate Baud: 9600	SRAMData Stack size:4072bytesProgram SRAM size:10 •KwordsData SRAM size:16288bytes	
Memory Model C Tiny Small Optimize for Speed Speed	Compilation Bit Variables size: 16 Promote char to int Char is unsigned Global #define Enhanced Instr. Automatic Register Allocation Use an External Startup Initialization File Enable Warnings Stack End Markers File Output Format(s): COFF, ROM, EEP	
	✓ OK ✓ OK ✓ OK ✓ OK ✓ OK ✓ OK	

The size of the bit variables, which are placed in registers R2 to R15, can be specified using the **Bit Variables size** list box.

Checking the **Promote char to int** check box enables the ANSI promotion of **char** operands to **int**. This option can also be specified using the **#pragma promotechar** compiler directive. Promoting **char** to **int** leads to increased code size and lower speed for an 8 bit chip microcontroller like the AVR.

If the **char is unsigned** check box is checked, the compiler treats by default the **char** data type as an unsigned 8 bit in the range 0...255.

If the check box is not checked the **char** data type is by default a signed 8 bit in the range -128...127. This option can also be specified using the **#pragma uchar** compiler directive.

Treating **char** as unsigned leads to better code size and speed.

Checking the **Global #define** check box allows #defined macros to be visible in all the project files.

The **Enhanced Instructions** check box allows enabling or disabling the generation of Enhanced Core instructions for the ATmega128, ATmega16, ATmega161, ATmega163, ATmega32, ATmega323, ATmega8 and AT94K FPSLIC devices.

The rest of the registers in the range R2 to R15, not used for bit variables, can be automatically allocated to **char** and **int** global variables by checking the **Compilation**|Automatic Register Allocation check box.

An external startup file can be used by checking the **Compilation|Use an External Startup File** check box.

The generation of warning messages during compilation can be enabled or disabled by using the **Compilation|Enable Warnings** check box.

For debugging purposes you have the option **Stack End Markers**. If you select it, the compiler will place the strings **DSTACKEND**, respectively **HSTACKEND**, at the end of the **Data Stack**, respectively **Hardware Stack** areas.

When you debug the program with the AVR Studio debugger you may see if these strings are overwritten, and consequently modify the **Data Stack Size**.

When your program runs correctly you may disable the placement of the strings in order to reduce code size.

Using the **File Output Format(s)** list box you can select one of the following formats for the files generated by the compiler:

- Intel HEX;
- COFF (required by the Atmel AVR Studio debugger), ROM and EEP (required by the In-System Programmer) ;
- Atmel generic OBJ, ROM and EEP (required by the In-System Programmer).

If the COFF file format is selected and the **Use the Terminal I/O in AVR Studio** check box is checked, special debugging information is generated in order to use the AVR Studio Terminal I/O window for communication with the simulated AVR chip's UART.

Changes can be saved, respectively canceled, using the OK, respectively Cancel buttons.

2.2.4.3 Transferring the Compiled Program to the AVR Chip after Make

This option is available if you select the After Make tab in the Project Configure window.

Configure Project mult		×									
Files CCompiler Afte	r Make										
Program the Chip Execute User's Program											
Chip Programming Opt	tions										
FLASH Lock Bits		Fuse Bit(s):									
No Protection		CKSEL0=0									
C Programming <u>d</u> is	abled										
C Programming an	d ⊻erification disabled	BODEN=0 BODLEVEL=0 BOOTRST=0									
Boot Lock Bit 0	Boot Lock Bit 1	BOOTSZ0=0									
B01=1 B02=1	B11=1 B12=1	□ BOOTSZ1=0 □ EESAVE=0									
C B01=0 B02=1	C B11=0 B12=1	□ JTAGEN=0 □ OCDEN=0									
C B01=0 B02=0	C B11=0 B12=0	LOODEN									
C B01=1 B02=0	C B11=1 B12=0										
Check Signature I Check Erasure I Preserve EEPROM Verify											
	✓ <u>о</u> к	X <u>C</u> ancel ? <u>H</u> elp									

If you check the **Program the Chip** option, then after successful compilation/assembly your program will be automatically transferred to the AVR chip using the built-in Programmer software.

The following steps are executed automatically:

- Chip erasure
- FLASH and EEPROM blank check
- FLASH programming and verification
- EEPROM programming and verification
- Fuse and Lock Bits programming

You can select the type of the chip you wish to program using the **Chip** combo box.

If the chip you have selected has Fuse Bit(s) that may be programmed, then a supplementary **Fuse Bit(s)** check box will appear. Using this check box you can set various chip options, which are described in the Atmel data sheets.

If you wish to protect your program from copying, you must select the corresponding option using the **FLASH Lock Bits** radio box.

If you wish to check the chip's signature before programming you must use the **Check Signature** option.

To speed up the programming process you can uncheck the **Check Erasure** check box. In this case there will be no verification of the correctness of the FLASH erasure.

The **Preserve EEPROM** checkbox allows preserving the contents of the EEPROM during chip erasure.

To speed up the programming process you can uncheck the **Verify** check box. In this case there will be no verification of the correctness of the FLASH and EEPROM programming.

Changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

2.2.4.4 Running an User Specified Program after Make

This option is available if you select the **After Make** tab in the Project Configure window. If you check the **Execute User's Program** option, then a program, that you have previously specified, will be executed after the compilation/assembly process.

a	Con	figur	e Project	Mult	ile.prj							х
	Files		Compiler	_								
		1-	, combioi	J (1							
		ΓE	Program th	e Chip	<u> </u>	ecute Use	er's Progr	am ;	🖏 Prog	gram <u>S</u> e	ettings	24.2
					 ✓ 	<u>0</u> K	×	<u>C</u> ano	cel	?	<u>H</u> elp	

Using the **Program Settings** button you can modify the:

- Program Directory and File Name
- Program Command Line Parameters
- Program Working Directory

🕅 User Program Settings		×
Program Directory and FileName:		
Command Line Parameters:		
Working Directory:		8::-
	✓ <u>o</u> k	X Cancel

Changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

2.2.5 Obtaining an Executable Program

Obtaining an executable program requires the following steps:

- 1. Compiling the Project's C source file, using the CodeVisionAVR C Compiler, and obtaining an assembler source file
- 2. Assembling the assembler source file, using the Atmel AVR assembler AVRASM32.

Compiling a File, executes step 1. Making, executes step 1 and 2 for the main project file

2.2.5.1 Compiling the Project

To compile the Project you must use the **Project|Compile File** menu command, press the **F9** key or press the **Compile** button of the toolbar. The CodeVisionAVR C Compiler will be executed, producing an assembler source file with the .asm extension.

The assembly source file can be examined and modified by opening it with the **Editor**.

After the compilation an Information window will open showing the compilation results.

1 Information	×
Compiler	
Chip: AT90S8515 Memory model: Small Optimize for: Size Promote char to int: No char is unsigned: No Automatic register allocation: On Use AVR Studio Terminal I/O: No	
872 line(s) compiled No errors No warnings	
Bit variables size: 0 byte(s)	
Data Stack area: 60h to DFh Data Stack size: 128 byte(s)	
Global variables area: E0h to 1551 Global variables size: 118 byte(s)	n
Hardware Stack area: 156h to 25F Hardware Stack size: 266 byte(s)	h
<u> </u>	

Eventual compilation errors and/or warnings will be listed in the **Message** window located under the **Editor** window, or in the **Navigator** window.

Warning	g: C:\	CVAVI	R\EXA	MPLES'	\DS182	20\ds1	820.c(1	28): gla	obal sy	mbol 'a	ls1820	_set_a	alarm'	decla	.red, b	out neve	rused
•																	
3:10				Insert													

By double clicking on the error or warning message, the line with the problem will be highlighted. The size of the **Message** window can be modified using the horizontal slider bar placed between it and the **Editor** window.

2.2.5.2 Making the Project

To make the Project you must use the **Project|Make** menu command, press the **Shift+F9** keys or press the **Make** button of the toolbar. The CodeVisionAVR C Compiler will be executed, producing an assembler source file with the .asm extension.

The rest of the files added to the project will be automatically linked to the main project file.

Eventual compilation errors and/or warnings will be listed in the **Message** window located under the **Editor** window, or in the **Navigator** window.

Warning: C	MPLES\C)S1820\ds1(320.c(128):	global syml	ool 'ds1820,	_set_alarm	' declared,	, but never	used
•									
3:10	Insert								

By double clicking on the error or warning message, the line with the problem will be highlighted. If no errors were encountered, then the Atmel AVR assembler AVRASM32 will be executed, obtaining the output file type specified in **Project|Configure|C Compiler**.

After the make process is completed an **Information** window will open showing the compilation results.

Pressing the **Compiler** tab will display compilation results.

🚺 Informa	ıtion	X
Compiler	Assembler	Programmer
Optimize fo Promote c char is uns Automatic	odel: Small or: Size har to int: No	cation: On
872 line(s) No errors No warnin	·	
Bit variable	es size: 0 byt	te(s)
	k area: 60h to k size: 128 by	· · ·
	riables area: riables size: 1	
	Stack area: 1 Stack size: 2	
4	🔖 <u>P</u> rogram	X <u>C</u> ancel

Pressing the **Assembler** tab will display assembly results.

🚺 Informa	ation		×
Compiler	Assembler	Programmer	
Creating Creating	'ds1820.eep' 'ds1820.rom' 'ds1820.obj' 'ds1820.lst'		
	ng 'ds1820.as 'ds1820.inc'	m'	
Code Constants Unused	nemory usag :1070 word (dw/db):57 :0 words :1127 words	s words 3	
	/complete wit 'ds1820.eep'	h no errors.	
	锋 <u>P</u> rogram	X Cance	el

Pressing the **Programmer** tab will display the **Chip Programming Counter**, which shows how many times was the AVR chip programmed so far.

1 Information
Compiler Assembler Programmer
Compiler Assembler Programming Counter: 0
<u>Serogram</u> <u>Scancel</u>

Pressing the Set Counter button will open the Set Programming Counter window:

ł	🛿 Set Programming Counter 🛛 🗵			
	New Counter Value: 0			
	✓ <u>O</u> K			

This dialog window allows setting the new Chip Programming Counter value.

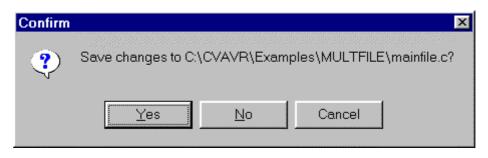
Pressing the **Program** button allows automatic programming of the AVR chip after successful compilation.

Pressing Cancel will disable automatic programming.

2.2.6 Closing a Project

You can quit working with the current Project by using the File|Close Project menu command.

If the Project files were modified, and weren't saved yet, you will be prompted if you want to do that.



Pressing **Yes** will save changes and close the project. Pressing **No** will close the project without saving the changes. Pressing **Cancel** will disable the project closing process.

When saving, the IDE will create a backup file with a **.pr~** extension.

2.3 Tools

Using the Tools menu you can execute other programs without exiting the CodeVisionAVR IDE.

2.3.1 The AVR Studio Debugger

The CodeVisionAVR C Compiler is designed to work in conjunction with the Atmel AVR Studio debugger version 3.50 or later.

Before you can invoke the debugger, you must first specify its location and file name using the **Settings|Debugger** menu command.

Debugger Directory a	and Filename			×
				-
	<mark>≊∷ B</mark> rowse	<u> </u>	X <u>C</u> ancel	

Changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

The debugger is executed by selecting the **Tools|Debugger** menu command or by pressing the **Debugger** button on the toolbar.

2.3.2 The AVR Chip Programmer

The CodeVisionAVR IDE has a built-in **In-System AVR Chip Programmer** that lets you easily transfer your compiled program to the microcontroller for testing.

The Programmer is designed to work with the Atmel STK500, Kanda Systems STK200+/300, Dontronics DT006, Vogel Elektronik VTEC-ISP or the MicroTronics ATCPU and Mega2000 development boards, connected to your computer's parallel printer port.

The Programmer is executed by selecting the **Tools|Chip Programmer** menu command or by pressing the **Chip Programmer** button on the toolbar.

· · · · · · · · · · · · · · · · · · ·	r <mark>ogrammer</mark> - d <u>C</u> ompare	<mark>- Kanda Systems STK200+/300 ⊠</mark> e <u>H</u> elp					
Chip: ATmega323 💌		C Reset Chip					
FLASH		EEPROM					
Start: 0 h End: 🤇	3FFF h	Start: 0 h End: 3FF h					
Checksum: 8000h		Checksum: FC00h					
Chip Programming Option FLASH Lock Bits © No Protection © Programming <u>d</u> isab © Programming and <u>v</u>	led	Fuse Bit(s):					
Boot Lock Bit 0	-Boot Lock B	Bit 1 BOOTSZ0=0					
B01=1 B02=1	B11=1 B1	B12=1 EESAVE=0					
C B01=0 B02=1	C B11=0 B1	B12=1 JTAGEN=0					
C B01=0 B02=0	C B11=0 B1	312=0					
C B01=1 B02=0	C B11=1 B1	312=0					
✓ Check Signature ✓ Check Erasure ✓ Preserve EEPROM ✓ Verify							

You can select the type of the chip you wish to program using the **Chip** combo box. If the chip you have selected has Fuse Bit(s) that may be programmed, then a supplementary **Fuse Bit(s)** check box will appear. Using this check box you can set various chip options, which are described in the Atmel data sheets. If you wish to protect your program from copying, you must select the corresponding option using the **FLASH Lock Bits** radio box.

The Programmer has two memory buffers:

- The FLASH memory buffer
- The EEPROM memory buffer.

You can Load or Save the contents of these buffers using the **File** menu. Supported file formats are:

- Atmel .rom and .eep
- Intel HEX
- Binary .bin

After loading a file in the corresponding buffer, the **Start** and **End** addresses are updated accordingly. You may also edit these addresses if you wish.

The contents of the FLASH, respectively EEPROM, buffers can be displayed and edited using the **Edit|FLASH**, respectively **Edit|EEPROM** menu commands.

When one of these commands is invoked, an Edit window displaying the corresponding buffer contents will open:

📓 E d	lit FL/	ASH B	luffer													키미	×
	x0	x1	x2	xЗ	x4	x5	x6	x7	x8	x9	хA	хB	хC	хD	хE	хF	•
000x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
001x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
002x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
003x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
004x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
005x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
006x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
007x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
008x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
009x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
00Ax	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
00Bx	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
00Cx	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
00Dx	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
00Ex	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
00Fx	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
010x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
011x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
012x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
013x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
014x	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	
1	FEEE	FFFF	FFFF		FFFF		FFFF	FFFF	FFFF	FFFF	FFFF				FFFF	· · · ·	

The buffer's contents, at the highlighted address, can be modified by typing in the new value. The highlighted address can be modified using the **arrow**, **Tab**, **Shift+Tab**, **PageUp** or **PageDown** keys. The Fill Memory Block window can be opened by right clicking in the Edit window:

8	Fill Memory	Block	×
	Start Address:	0	h
	End Address:	FF	h
	Fill Value:	0	h
			.1
	<u> </u>	X <u>C</u> anc	el

This window lets you specify the **Start Address**, **End Address** and **Fill Value** of the memory area to be filled.

If you wish to check the chip's signature before any operation you must use the **Check Signature** option.

To speed up the programming process you can uncheck the **Check Erasure** check box. In this case there will be no verification of the correctness of the FLASH erasure.

The **Preserve EEPROM** checkbox allows preserving the contents of the EEPROM during chip erasure.

To speed up the programming process you also can uncheck the **Verify** check box. In this case there will be no verification of the correctness of the FLASH and EEPROM programming.

For erasing a chip's FLASH and EEPROM you must select the **Program|Erase** menu command. After erasure the chip's FLASH and EEPROM are automatically blank checked. For simple blank checking you must use the **Program|Blank Check** menu command. If you wish to program the FLASH with the contents of the FLASH buffer you must use the

Program|FLASH menu command.

For programming the EEPROM you must use the **Program**|**EEPROM** menu command. After programming the FLASH and EEPROM are automatically verified.

To program the Lock, respectively the Fuse Bit(s) you must use the **Program|Fuse Bit(s)**, respectively **Program|Lock Bits** menu commands.

The Program|All menu command allows to automatically:

- Erase the chip
- FLASH and EEPROM blank check
- Program and verify the FLASH
- Program and verify the EEPROM
- Program the Fuse and Lock Bits.

If you wish to read the contents of the chip's FLASH, respectively EEPROM, you must use the **Read|FLASH**, respectively **Read|EEPROM** menu commands.

For reading the chip's signature you must use the **Read|Chip Signature** menu command. To read the Lock, respectively the Fuse Bits you must use the **Read|Lock Bits**, respectively **Read|Fuse Bits** menu commands.

For comparing the contents of the chip's FLASH, respectively EEPROM, with the corresponding memory buffer, you must use the **Compare|FLASH**, respectively **Compare|EEPROM** menu commands.

For exiting the Programmer and returning to the CodeVisionAVR IDE you must use the **File|Close** menu command.

2.3.3 The Serial Communication Terminal

The **Terminal** is intended for debugging embedded systems, which employ serial communication (RS232, RS422, RS485).

The Terminal is invoked using the **Tools**|**Terminal** menu command or the **Terminal** button on the toolbar.

💻 Terminal						
Hex Code:	<u>S</u> end	<u>B</u> x File	<u>T</u> x File	He <u>x</u>	<u>C</u> lear	CReset Chip
						-
						_
•						
COM2: 9600,8N1	No handsh.	ASCI	TY Echo	on		

The characters can be displayed in ASCII or hexadecimal format. The display mode can be toggled using the **Hex/ASCII** button.

The received characters can be saved to a file using the **Rx File** button.

Any characters typed in the Terminal window will be transmitted through the PC serial port. The entered characters can be deleted using the **Backspace** key.

By pressing the **Send** button, the Terminal will transmit a character whose hexadecimal ASCII code value is specified in the Hex Code edit box.

By pressing the Tx File button, the contents of a file can be transmitted through the serial port.

By pressing the **Reset** button, the AVR chip on the STK200+/300, VTEC-ISP, DT006, ATCPU or Mega2000 development board is reseted.

At the bottom of the Terminal window there is a status bar in which are displayed the:

- computer's communication port;
- communication parameters;
- handshaking mode;
- received characters display mode;
- type of emulated terminal;
- the state of the transmitted characters echo setting.

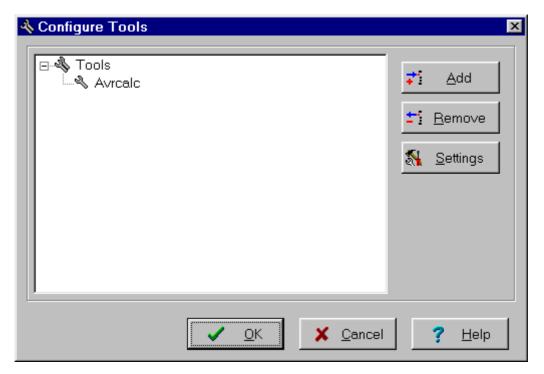
2.3.4 Executing User Programs

User programs are executed by selecting the corresponding command from the **Tools** menu. You must previously add the Program's name to the menu.

2.3.4.1 Configuring the Tools Menu

You can add or remove User Programs from the **Tools** menu by using the **Tools|Configure** menu command.

A Configure Tools dialog window, with a list of User Programs, will open.



Using the **Add** button you can add a Program to the **Tools** menu. Using the **Remove** button you can remove a Program from the **Tools** menu. Using the **Settings** button you can modify the:

- Tool Menu Name
- Tool Directory and File Name
- Command Line Parameters
- Working Directory of a selected Program from the list.

💦 Tool Settings	×
Tool Name:	
Avrcalc	
Tool Directory and FileName:	
C:\CVAVR\BIN\Avrcalc.exe	
Command Line Parameters:	
Working Directory:	
C:\CVAVR\BIN\	
	✓ <u>O</u> K X <u>C</u> ancel

Changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

2.4 IDE Settings

The CodeVisionAVR IDE is configured using the **Settings** menu.

2.4.1 General Settings

The General Settings can be configured using the Settings|General menu command.



If the **Show Toolbar** check box is checked the command buttons toolbar will be displayed.

If the **Show Navigator** check box is checked the **Navigator** window is displayed in the left of the main program window.

If the **Show Information** check box is checked, there will be an **Information** window displayed after **Compiling** or **Making**.

The **General Settings** changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

2.4.2 Configuring the Editor

The Editor can be configured using the Settings|Editor menu command.

Editor Settings			×
☑ Syntax Highlighting	Colors		_
	Background:	🗖 Default 💽	
Show Line <u>N</u> umbers	Text:	🗖 Default 💌]
✓ Autoindent	C Keywords:	🗖 Default 💌]
	Preprocessor:	📕 Default 💽]
Tab Size: 4 🏒	Storage modif.:	📕 Default 💽]
	Strings:	📕 Default 🖉]
Font: A	Comments:	🗖 Default 💌]
			1
		🗙 <u>C</u> ancel 🛛 훪 <u>D</u> efa	ult

By checking or unchecking the **Syntax Highlighting** check box, you can enable or disable the C syntax color highlighting of the files displayed in the Editor windows.

By checking or unchecking the **Show Line Numbers** check box, you can enable or disable the displaying of the line numbers in the Editor windows.

By checking or unchecking the **Autoindent** check box, you can enable or disable the autoindenting during file editing.

The number of blank spaces, inserted when pressing the **Tab** key, can be specified using the **Tab Size** spin edit.

The font, used by the text Editor and the Terminal, can be specified using the Font button.

The different colors, used for displaying the text in the **Editor** windows and for C syntax highlighting, can be specified by clicking on the appropriate panels in the **Colors** group. The **Background** and **Text** color settings are the same for both the EditorEdit File and the TerminalTerminal.

The **Editor** configuration changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

By pressing the **Default** button the default **Editor** settings are restored.

2.4.3 Configuring the Assembler

The Assembler can be configured using the Settings|Assembler menu command.

Ass	sembler Settings	×
	On Assembler Error	
	O Open the . <u>a</u> sm file	
	✓ <u>O</u> K X <u>C</u> ancel	

The **On Assembler Error** options allow to select which file will be automatically opened by the **Editor** in the case of an assembly error.

The **Assembler** configuration changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

2.4.4 Setting the Debugger Path

The CodeVisionAVR C Compiler is designed to work in conjunction with the Atmel AVR Studio debugger version 3.50 or later.

Before you can invoke the debugger, you must first specify its location and file name using the **Settings|Debugger** menu command.

Debugger Directory	and Filename			×
	<mark>≌∷ B</mark> rowse	<u>o</u> k	X <u>C</u> ancel	

Changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

Pressing the **Browse** button opens a dialog window that allows selecting the debugger's directory and filename.

Debugger Dire	ctory and FileName	? ×
Look <u>i</u> n: 🔂 A	VRstd3 💽 🖻 📶 🛅	
Appnotes AvrProg Help Ice200 IOSetup Selftest Upgrade131	'≱ A∨rStudio	
File <u>n</u> ame:	AvrStudio Open	
Files of <u>type</u> :	Executables (*.exe) Cancel	

2.4.5 AVR Chip Programmer Setup

Using the **Settings**|**Programmer** menu command, you can select the type of the in-system programmer that is used, and the computer's port to which the programmer is connected. The current version of CodeVisionAVR supports the following in-system programmers:

- Kanda Systems STK200+ and STK300
- Atmel STK500
- Dontronics DT006
- Vogel Elektronik VTEC-ISP
- MicroTronics ATCPU and Mega2000

The STK200+, STK300, DT006, VTEC-ISP, ATCPU and Mega2000 in-system programmers use the parallel printer port.

The following choices are available through the **Printer Port** radio group box:

- LPT1, at base address 378h;
- LPT2, at base address 278h;
- LPT3, at base address 3BCh.

Programmer Settings 🛛 🔀
AVR Chip Programmer Type:
Kanda Systems STK200+/300 💌
Printer Port
C LPT1 (378h)
LPT2 (278h)
C LPT3 (3BCh)
✓ <u>O</u> K ✓ <u>C</u> ancel

The STK500 in-system programmer is supported through the **STK500.EXE** command line utility supplied with AVRStudio 3.50 or later.

In order to use this utility you must specify the directory where it is located.

This is done using the STK500.EXE Directory button.

The STK500 programmer uses the RS232C serial communication port, which can be specified using the **Communication Port** list box.

Programmer Settings	×
AVR Chip Programmer Type:	
Atmel STK500	-
È: STK500.EXE Directory	
Communication Port: COM2	•
<u>Q</u> K <u>C</u> ance	el

Changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

2.4.6 Serial Communication Terminal Setup

The serial communication **Terminal** is configured using the **Settings|Terminal** menu command.

Te	erminal Set	tings			×
	Port: Baud rate: Data bits: Stop bits:	COM2 9600 8 1	• • •	Handshaking ⓒ None ⓒ Xon/Xoff ⓒ RTS/CTS ⓒ DTR/DSR Append LF ☑ On Reception	
	Parity:	None	-	✓ On <u>Transmission</u>	1
	Emulation:	TTY	•	Echo	
		<u>i</u> K	🗙 <u>C</u> ar	ncel ? <u>H</u> elp	

In the Terminal Setup window you can select the:

- computer's communication port used by the Terminal: COM1 to COM6;
- Baud rate used for communication: 110 to 115200;
- number of data bits used in reception and transmission: 5 to 8;
- number of stop bits used in reception and transmission: 1, 1.5 or 2;
- parity used in reception and transmission: None, Odd, Even, Mark or Space;
- type of emulated terminal: TTY, VT52 or VT100;
- type of handshaking used in communication: None, Hardware (CTS or DTR) or Software (XON/XOFF);
- possibility to append LF characters after CR characters on reception and transmission;
- enabling or disabling the echoing of the transmitted characters.

Changes can be saved, respectively canceled, using the **OK**, respectively **Cancel** buttons.

2.5 Accessing the Help

CodeVisionAVR help system is accessed by invoking the **Help|Help** menu command or by pressing the **Help** toolbar button.

2.6 Transferring the License to another computer

The CodeVisionAVR C compiler features a computer locked licensing system.

This means that, the first time after purchase, you will receive from the author a license file that is specific to your particular computer.

This prevents you from using the software on another computer, until you will **Export** the license to this computer.

After the license **Export**, the compiler on the first computer will be disabled and you will only be able to run the compiler on the second one.

You can always **Export** the license back to the first computer, but the license on the second one will be disabled after that.

By this procedure only one person can use the compiler at a time.

To **Export** the license from the computer #1 to the computer #2 you must proceed like this:

- install the CodeVisionAVR C compiler on the computer #2
- execute the compiler on computer #2, it will display a specific serial number



- execute the compiler on computer #1 and select the Help|Export menu command, an Export CodeVisionAVR License dialog window will open
- enter the serial number from computer #2 in the Destination Serial Number edit box

Export CodeVisionAVR License	e 🗵
Serial # of the destination computer	c III
5AD5-4F7C-ECD7-E225	-
	1
Export X Cancel	

• press the Export button, if you press the Cancel button the Export will be canceled

• you will be prompted where to place the new license file for the computer #2, usually you can chose a diskette in drive A:

.
•

• press **Save**. After the new license file is successfully copied to the diskette, the compiler on computer #1 will cease running

• place the diskette with the license file in drive A: of computer #2 and press the **Import** button. After that the license transfer is completed and the compiler will run only on computer #2.



Please note that after the **Export** procedure, the serial number of the computer #1 will change. In this situation when you will try to **Import** a license back to this computer, you must enter this <u>new</u> serial number, <u>not</u> the old one.

2.7 Connecting to HP InfoTech's Web Site

The Help|HP InfoTech Web Site menu command opens the default web browser and connects to HP InfoTech's web site http://infotech.ir.ro

Here you can check for the latest HP InfoTech's products and updates to CodeVisionAVR.

2.8 Contacting HP InfoTech by E-Mail

The **Help|E-Mail HP InfoTech** menu command opens the default e-mail program and allows you to send an e-mail to: **hpinfotech@xnet.ro** and **dhptechn@ir.ro**.

2.9 Quitting the CodeVisionAVR IDE

To quit working with the CodeVisionAVR IDE you must select the **File|Exit** menu command. If some source files were modified and were not saved yet, you will be prompted if you want to do that.

3. CodeVisionAVR C Compiler Reference

This section describes the general syntax rules for the CodeVisionAVR C compiler.

Only specific aspects regarding the implementation of the C language by this compiler are exposed. This help is not intended to teach you the C language; you can use any good programming book to do that.

You must also consult the appropriate AVR data sheets from Atmel.

3.1 The Preprocessor

The Preprocessor directives allows you to:

- include text from other files, such as header files containing library and user function prototypes
- define macros that reduce programming effort and improve the legibility of the source code
- set up conditional compilation for debugging purposes and to improve program portability
- issue compiler specific directives

The **#include** directive may be used to include another file in your source. You may nest as many as 16 **#include** files. Example:

```
/* File will be looked for in the /inc directory of the compiler. */
#include <file_name>
```

or

```
/* File will be looked for in the current project directory.
    If it's not located there, then it will be included from
    the /inc directory of the compiler. */
#include "file name"
```

The **#define** directive may be used to define a macro. Example:

```
#define ALFA 0xff
```

This statement defines the symbol 'ALFA' to the value 0xff. The C preprocessor will replace 'ALFA' with 0xff in the source text before compiling.

Macros can also have parameters. The preprocessor will replace the macro with it's expansion and the formal parameters with the real ones. Example:

```
#define SUM(a,b) a+b
/* the following code sequence will be replaced with int i=2+3; */
int i=SUM(2,3);
```

When defining macros you can use the **#** operator to convert the macro parameter to a character string. Example:

```
#define PRINT MESSAGE(t) printf(#t)
```

```
/* ..... */
/* the following code sequence will be replaced with printf("Hello"); */
PRINT MESSAGE(Hello);
```

Two parameters can be concatenated using the **##** operator. Example:

```
#define ALFA(a,b) a ## b
```

```
/* the following code sequence will be replaced with char xy=1; */ char ALFA(x,y)=1;
```

A macro definition can be extended to a new line by using \ . Example:

```
#define MESSAGE "This is a very \
long text..."
```

A macro can be undefined using the **#undef** directive. Example:

#undef ALFA

The **#ifdef**, **#ifndef**, **#else** and **#endif** directives may be used for conditional compilation. The syntax is:

```
#ifdef macro_name
[set of statements 1]
#else
[set of statements 2]
#endif
```

If 'alfa' is a defined macro name, then the **#ifdef** expression evaluates to true and the set of statements 1 will be compiled.

Otherwise the set of statements 2 will be compiled. The **#else** and set of statements 2 are optional. If 'alfa' is not defined, the **#ifndef** expression evaluates to true. The rest of the syntax is the same as that for **#ifdef**.

The #if, #elif, #else and #endif directives may be also used for conditional compilation.

```
#if expression1
[set of statements 1]
#elif expression2
[set of statements 2]
#else
[set of statements 3]
#endif
```

If **expression1** evaluates to true, the set of statements 1 will be compiled. If **expression2** evaluates to true, the set of statements 2 will be compiled. Otherwise the set of statements 3 will be compiled. The **#else** and set of statements 3 are optional. There are the following predefined macros:

LINE___ the current line number of the compiled file

FILE the current compiled file

______TIME___ the current time in *hh:mm:ss* format _______DATE__ the current date in *mmm dd yyyy* format ______MODEL__TINY_ if the program is compiled using the TINY memory model

_MODEL_SMALL_ if the program is compiled using the SMALL memory model **_OPTIMIZE_SIZE_** if the program is compiled with optimization for size

_OPTIMIZE_SPEED_ if the program is compiled with optimization for speed

_UNSIGNED_CHAR_ if the char is unsigned compiler option is enabled or #pragma uchar+ is used

GLOBAL DEFINES if the Global #define compiler option is enabled.

The **#line** directive can be used to modify the predefined **LINE** and **FILE** macros. The syntax is:

#line integer constant ["file name"]

Example:

```
/* This will set _LINE__ to 50 and
__FILE__ to "file2.c" */
#line 50 "file2.c"
/* This will set LINE to 100 */
#line 100
```

There **#error** directive can be used to stop compilation and display an error message. The syntax is:

#error error message

Example:

#error This is an error!

The **#pragma** directive allows compiler specific directives. You can use the **#pragma warn** directive to enable or disable compiler warnings. Example:

```
/* Warnings are disabled */
#pragma warn-
/* Write some code here */
/* Warnings are enabled */
#pragma warn+
```

The compiler's code optimizer can be turned on or off using the **#pragma opt** directive. This directive must be placed at the start of the source file. The default is optimization turned on. Example:

```
/* Turn optimization off, for testing purposes */
#pragma opt-
```

or

```
/* Turn optimization on */
#pragma opt+
```

If the code optimization is enabled, you can optimize some portions or all the program for size or speed using the **#pragma optsize** directive.

The default state is determined by the **Project|Configure|C Compiler|Compilation|Optimization** menu setting. Example:

```
/* The program will be optimized for minimum size */
#pragma optsize+
/* Place your program functions here */
/* Now the program will be optimized for maximum execution speed */
#pragma optsize-
```

/* Place your program functions here */

The automatic saving and restoring of registers R0, R1, R22, R23, R24, R25, R26, R27, R30, R31 and SREG, during interrupts can be turned on or off using the **#pragma savereg** directive. Example:

```
/* Turn registers saving off */
#pragma savereg-
/* interrupt handler */
interrupt [1] void my irg(void) {
/* now save only the registers that are affected by the routines in the
  handler, for example R30, R31 and SREG */
#asm
   push r30
   push r31
    in r30, SREG
   push r30
#endasm
/* place the C code here */
/* now restore SREG, R31 and R30 */
#asm
   pop r30
   out SREG,r30
   pop r31
   pop r30
#endasm
}
/* re-enable register saving for the other interrupts */
#pragma savereg+
```

The default state is automatic saving of registers during interrupts.

The automatic allocation of global variables to registers can be turned on or off using the **#pragma regalloc** directive.

The default state is determined by the **Project|Configure|C Compiler|Compilation|Automatic Register Allocation** check box.

Example:

```
/* the following global variable will be automatically
allocated to a register */
#pragma regalloc+
unsigned char alfa;
/* the following global variable will not be automatically
allocated to a register and will be placed in normal SRAM */
#pragma regalloc-
```

unsigned char beta;

The ANSI **char** to **int** operands promotion can be turned on or off using the **#pragma promotechar** directive. Example:

```
/* turn on the ANSI char to int promotion */
#pragma promotechar+
/* turn off the ANSI char to int promotion */
#pragma promotechar-
```

This option can also be specified in the Project|Configure|C Compiler|Promote char to int menu.

Treating **char** by default as an unsigned 8 bit can be turned on or off using the **#pragma uchar** directive. Example:

```
/* char will be unsigned by default */
#pragma uchar+
/* char will be signed by default */
#pragma uchar-
```

This option can also be specified in the Project|Configure|C Compiler|char is unsigned menu.

The **#pragma library** directive is used for specifying the necessity to compile/link a specific library file. Example:

#pragma library mylib.lib

3.2 Comments

The character string "/*" marks the beginning of a comment. The end of the comment is marked with "*/". Example:

```
/* This is a comment */
/* This is a
    multiple line comment */
```

One-line comments may be also defined by using the string "//". Example:

 $\ensuremath{{//}}$ This is also a comment

Nested comments are not allowed.

3.3 Reserved Keywords

Following is a list of keywords reserved by the compiler. These can not be used as identifier names.

break bit case char const continue default defined do double eeprom else enum extern flash float for funcused goto if inline int interrupt long register return short signed sizeof sfrb sfrw static struct switch typedef union unsigned void volatile while

3.4 Identifiers

An identifier is the name you give to a variable, function, label or other object. An identifier can contain letters (A...Z, a...z) and digits (0...9), as well as the underscore character (_).

However an identifier can only start with a letter or an underscore.

Case is significant; i.e. variable1 is not the same as Variable1.

Identifiers can have up to 32 characters.

3.5 Data Types

The following table lists all the data types supported by the CodeVisionAVR C compiler, their range of possible values and their size:

Туре	Size (Bits)	Range
bit	1	0,1
char	8	-128 to 127
unsigned char	8	0 to 255
signed char	8	-128 to 127
int	16	-32768 to 32767
short int	16	-32768 to 32767
unsigned int	16	0 to 65535
signed int	16	-32768 to 32767
long int	32	-2147483648 to 2147483647
unsigned long int	32	0 to 4294967295
signed long int	32	-2147483648 to 2147483647
float	32	±1.175e-38 to ±3.402e38
double	32	±1.175e-38 to ±3.402e38

The **bit** data type is supported only for global variables.

If the **Project|Configure|C Compiler|char is unsigned** option is checked or **#pragma uchar+** is used, then **char** has by default the range 0..255.

3.6 Constants

Integer or long integer constants may be written in decimal form (e.g. 1234), in binary form with **0b** prefix (e.g. 0b101001), in hexadecimal form with **0x** prefix (e.g. 0xff) or in octal form with **0**-prefix (e.g. 0777).

Unsigned integer constants may have the suffix **U** (e.g. 10000U). Long integer constants may have the suffix **L** (e.g. 99L). Unsigned long integer constants may have the suffix **UL** (e.g. 99UL). Floating point constants may have the suffix **F** (e.g. 1.234F). Character constants must be enclosed in single quotation marks. E.g. 'a'.

String constants must be enclosed in double quotation marks. E.g. "Hello world". If you place a string between quotes as a function parameter, this string will automatically be considered as constant and will be placed in FLASH memory. Example:

```
/ * this function displays a string located in SRAM */
void display ram(char *s) {
/* ..... */
}
/ * this function displays a string located in FLASH */
void display flash(char flash *s) {
/* ..... */
}
void main(void) {
/* this will not work !!! */
/* because the function addresses the string as */
/* it is located in SRAM, but the string "Hello world" */
/* is constant and is placed in FLASH */
display ram("Hello world");
/* this will work !!! */
/* the function addresses the string as it is located in FLASH */
display flash("Hello world");
}
```

Constant can be grouped in arrays, which can have up to 8 dimensions. Constants are stored in FLASH memory, to specify this you must use the **flash** or **const** keywords. Constant expressions are automatically evaluated during compilation. Example:

```
flash int integer_constant=1234+5;
flash char char_constant='a';
flash long long_int_constant1=99L;
flash long long_int_constant2=0x10000000;
flash int integer_array1[]={1,2,3};
/* The first two elements will be 1 and 2,
    the rest will be 0 */
flash int integer_array2[10]={1,2};
flash int multidim_array[2,3]={{1,2,3},{4,5,6}};
flash char string_constant1[]="This is a string constant";
const char string_constant2[]="This is also a string constant";
```

Constants can't be declared inside functions.

3.7 Variables

Program variables can be global (accessible to all the functions in the program) or local (accessible only inside the function they are declared).

If not specifically initialized, the global variables are automatically set to 0 at program startup. The local variables are not automatically initialized on function call. The syntax is:

[<storage modifier>] <type definition> <identifier>;

Example:

```
/* Global variables declaration */
char a;
int b;
/* and initialization */
long c=111111;
void main(void) {
   /* Local variables declaration */
char d;
int e;
   /* and initialization */
long f=2222222;
}
```

Variables can be grouped in arrays, which can have up to 8 dimensions. The first element of the array has always the index 0. If not specifically initialized, the elements of global variable arrays are automatically set to 0 at program startup. Example:

```
/* All the elements of the array will be 0 */
int global array1[32];
/* Array is automatically initialized */
int global array2[]=\{1, 2, 3\};
int global array3[4]={1,2,3,4};
char global array4[]="This is a string";
/* Only the first 3 elements of the array are
   initialized, the rest 29 will be 0 */
int global array5[32]={1,2,3};
/* Multidimensional array */
int multidim array[2,3]={{1,2,3},{4,5,6}};
void main(void) {
/* local array declaration */
int local array1[10];
/* local array declaration and initialization */
int local array2[3]={11,22,33};
char local array3[7]="Hello";
}
```

Local variables that must conserve their values during different calls to a function must be declared as **static**. Example:

```
int alfa(void) {
    /* declare and initialize the static variable */
static int n=1;
return n++;
}
void main(void) {
    int i;
    /* the function will return the value 1 */
    i=alfa();
    /* the function will return the value 2 */
    i=alfa();
}
```

If not specifically initialized, static variables are automatically set to 0 at program startup.

Variables that are declared in other files must be preceded by the **extern** keyword. Example:

extern int xyz;

```
/* now include the file which contains
   the variable xyz definition */
#include <file_xyz.h>
```

To instruct the compiler to allocate a variable to registers, the **register** modifier must be used. Example:

register int abc;

The compiler may automatically allocate a variable to registers, even if this modifier is not used.

The **volatile** modifier must be used in order to prevent a variable to be allocated to registers and to warn the compiler that it may be subject to outside change during evaluation. Example:

volatile int abc;

All the global variables, not allocated to registers, are stored in the **Global Variables** area of SRAM. All the local variables, not allocated to registers, are stored in dynamically allocated space in the **Data Stack** area of SRAM.

3.7.1 Specifying the SRAM Storage Address for Global Variables

Global variables can be stored at specific SRAM locations at design-time using the @ operator. Example:

```
/* the integer variable "a" is stored
    in SRAM at address 80h */
int a @0x80;
/* the structure "alfa" is stored
    in SRAM at address 90h */
struct x {
        int a;
        char c;
        } alfa @0x90;
```

3.7.2 Bit Variables

The bit variables are special global variables located in the register R2 to R15 memory space. These variables are declared using the **bit** keyword. The syntax is:

```
bit <identifier>;
```

Example:

```
/* declaration and initialization */
bit alfa=1; /* bit0 of R2 */
bit beta; /* bit1 of R2 */
void main(void)
{
  if (alfa) beta=!beta;
  /* ...... */
}
```

Memory allocation for the bit variables is done, in the order of declaration, starting with bit 0 of R2, then bit 1 of R2 and so on, in ascending order.

A number of maximum 112 bit variables can be declared.

The size of the bit variables allocated to the program can be specified in the **Project|Configure|C Compiler|Compilation|Bit Variables Size** list box.

This size should be as low as possible, in order to free registers for allocation to other global variables. If not specifically initialized, the bit global variables are automatically set to 0 at program startup. In expression evaluation bit variables are automatically promoted to **unsigned char**.

3.7.3 Allocation of Variables to Registers

In order to fully take advantage of the AVR architecture and instruction set, the compiler allocates some of the program variables to chip registers.

The registers from R2 up to R15 can be allocated for bit variables.

You may specify how many registers in this range are allocated using the **Project|Configure|C Compiler|Compilation|Bit Variables Size** list box. This value must be as low as required by the program.

If the **Project|Configure|C Compiler|Compilation|Automatic Register Allocation** option is checked or the **#pragma regalloc+** compiler directive is used, the rest of registers in the R2 to R15 range, that aren't used for **bit** variables, are allocated to **char** and **int** global variables. The allocation is realized in order of variable declaration until the R15 register is allocated.

If the automatic register allocation is disabled, you can use the **register** keyword to specify which global variable to be allocated to registers.

Example:

/* disable automatic register allocation */
#pragma regalloc/* allocate the variable 'alfa' to a register */
register int alfa;
/* allocate the variable 'beta' to the register pair R14, R15 */
register int beta @14;

The **char** and **int** local variables are automatically allocated, in order of declaration, to registers R16 up to R21.

3.7.4 Structures

Structures are user-defined collections of named members. The structure members can be any of the supported data types, arrays of these data types or pointers

to them. Structures are defined using the **struct** reserved keyword. The syntax is:

```
[<storage modifier>] struct [<structure tag-name>] {
          [<type> <variable-name[, variable-name, ...]>];
          [<type> <variable-name[, variable-name, ...]>];
          } [<structure variables>];
Example:
/* Global structure located in SRAM */
struct ram structure {
             char a,b;
             int c;
             char d[30],e[10];
             char *pp;
             } sr;
/* Global constant structure located in FLASH */
flash struct flash structure {
             int a;
             char b[30], c[10];
             } sf;
/* Global structure located in EEPROM */
eeprom struct eeprom structure {
             char a;
             int b;
             char c[15];
             } se;
void main(void) {
/* Local structure */
struct local structure {
             char a;
             int b;
             long c;
             } sl;
/* .....*/
}
```

The space allocated to the structure in memory is equal to sum of the sizes of all the members.

There are some restrictions that apply to the structures stored in FLASH and EEPROM. Due to the fact that pointers must be always located in SRAM, they can't be used in these structures.

Because with the Atmel AVRASM32 Assembler single bytes defined with .DB in FLASH occupy in reality 2 bytes, the CodeVisionAVR C compiler will replace the **char** members of structures stored in FLASH with **int**.

Also it will extend the size of the **char** arrays, members of such structures, to an even value.

Structures can be grouped in unidimensional arrays. Example how to initialize and access an global structure array stored in EEPROM:

```
/* Global structure array located in EEPROM */
eeprom struct eeprom structure {
             char a;
             int b;
             char c[15];
             } se[2]={{'a',25,"Hello"},
                     { 'b',50, "world" } };
void main(void) {
char k1, k2, k3, k4;
int i1, i2;
/* define a pointer to the structure */
struct eeprom structure eeprom *ep;
/* direct access to structure members */
k1=se[0].a;
i1=se[0].b;
k2=se[0].c[2];
k3=se[1].a;
i2=se[1].b;
k4=se[1].c[2];
/* same access to structure members using a pointer */
ep=&se; /* initialize the pointer with the structure address */
k1=ep->a;
il=ep->b;
k2=ep->c[2];
++ep;
      /* increment the pointer */
k3 = ep - a;
i2=ep->b;
k4=ep->c[2];
}
```

Because some AVR devices have a small amount of SRAM, in order to keep the size of the **Data Stack** small, structures can't be passed directly as function parameters. Structures can be passed as function parameters or returned by functions only by using pointers. Example:

```
struct alpha {
              int a,b, c;
              } s={2,3};
/* define the function */
struct alpha *sum struct(struct alpha *sp) {
/* member c=member a + member b */
sp->c=sp->a + sp->b;
/\,\star\, return a pointer to the structure \,\star/\,
return sp;
}
void main(void) {
int i;
/* s->c=s->a + s->b */
/* i=s->c */
i=sum struct(&s)->c;
}
```

Bitfields in structures are not implemented. For bit level storage use bit variables.

3.7.5 Unions

Unions are user-defined collections of named members that share the same memory space. The union members can be any of the supported data types, arrays of these data types or pointers to them.

Unions are defined using the **union** reserved keyword. The syntax is:

```
[<storage modifier>] union [<union tag-name>] {
    [<type> <variable-name[, variable-name, ...]>];
    [<type> <variable-name[, variable-name, ...]>];
    ...
    } [<union variables>];
```

Unions are always stored in SRAM.

The space allocated to the union in memory is equal to the size of the largest member. Union members can be accessed in the same way as structure members. Example:

```
/* union declaration */
union alpha {
            unsigned char lsb;
            unsigned int word;
            } data;
void main(void) {
unsigned char k;
/* define a pointer to the union */
union alpha *dp;
/* direct access to union members */
data.word=0x1234;
k=data.lsb; /* get the LSB of 0x1234 */
/* same access to union members using a pointer */
dp=&data; /* initialize the pointer with the union address */
dp->word=0x1234;
k=dp->lsb; /* get the LSB of 0x1234 */
```

}

Because some AVR devices have a small amount of SRAM, in order to keep the size of the **Data Stack** small, unions can't be passed directly as function parameters. Unions can be passed as function parameters or returned by functions only by using pointers. Example:

3.7.6 Enumerations

The enumeration data type can be used in order to provide mnemonic identifiers for a set of **int** values.

```
The enum keyword is used for this purpose. The syntax is:
```

```
[<storage modifier>] enum [<enum tag-name>] {
    [<constant-name[[=constant-initializer], constant-name, ...]>]}
    [<enum variables>];
```

Example:

```
/* The enumeration constants will be initialized as follows:
   sunday=0 , monday=1 , tuesday=2 ,..., saturday=6 */
enum days {
          sunday, monday, tuesday, wednesday,
          thursday, friday, saturday} days of week;
/* The enumeration constants will be initialized as follows:
   january=1 , february=2 , march=3 ,..., december=12 */
enum months {
            january=1, february, march, april, may, june,
            july, august, september, october, november, december}
            months of year;
void main {
/\star the variable days of week is initialized with
   the integer value 6 */
days of week=saturday;
}
```

Enumerations can be stored in SRAM or EEPROM. To specify the storage in EEPROM, the **eeprom** keyword must be used. Example:

eeprom enum days {
 sunday, monday, tuesday, wednesday,
 thursday, friday, saturday} days of week;

3.7.7 Global Variables Memory Map File

During compilation the C compiler generates a Global Variables Memory Map File, in which are specified the SRAM address location, register allocation and size of the global variables used by the program.

This file has the **.map** extension and can be viewed using the menu **File|Open** command or by pressing the **Open** button on the toolbar.

Structure and union members are listed individually along with their corresponding address and size. This file is useful during program debugging using the AVR Studio debugger.

3.8 Defining Data Types

User defined data types are declared using the **typedef** reserved keyword. The syntax is:

```
typedef [<storage modifier>] <type definition> <identifier>;
```

The symbol name <identifier> is assigned to <type definition>. Examples:

3.9 Type Conversions

In an expression, if the two operands of a binary operator are of different types, then the compiler will convert one of the operands into the type of the other. The compiler uses the following rules:

If either of the operands is of type float then the other operand is converted to the same type.

If either of the operands is of type **long int** or **unsigned long int** then the other operand is converted to the same type.

Otherwise, if either of the operands is of type **int** or **unsigned int** then the other operand is converted to the same type.

Thus char type or unsigned char type gets the lowest priority.

```
Using casting you can change these rules.
Example:
void main(void) {
  int a, c;
  long b;
/* The long integer variable b will be treated here as an integer */
  c=a+(int) b;
}
```

It is important to note that if the **Project|Configure|C Compiler|Promote char to int** option isn't checked or the **#pragma promotechar+** isn't used, the **char**, respectively **unsigned char**, type operands are not automatically promoted to **int**, respectively **unsigned int**, as in compilers targeted for 16 or 32 bit CPUs.

This helps writing more size and speed efficient code for an 8 bit CPU like the AVR. To prevent overflow on 8 bit addition or multiplication, casting may be required. The compiler issues warnings in these situations. Example:

```
void main(void) {
unsigned char a=30;
unsigned char b=128;
unsigned int c;
/* This will generate an incorrect result, because the multiplication
    is done on 8 bits producing an 8 bit result, which overflows.
    Only after the multiplication, the 8 bit result is promoted to
    unsigned int */
c=a*b;
/* Here casting forces the multiplication to be done on 16 bits,
    producing an 16 bit result, without overflow */
c=(unsigned int) a*b;
}
```

The compiler behaves differently for the following operators:

- += -= *= /= %= &= &= ^= ^=
- <<= >>=

For these operators, the result is to be written back onto the left-hand side operand (which must be a variable). So the compiler will always convert the right hand side operand into the type of left-hand side operand.

3.10 Operators

The compiler supports the following operators:

+	-
*	/
90	++
	=
==	~
!	! =
<	>
<=	>=
&	& &
^	?
<<	>>
-=	+=
/=	%=
~=	*=
^=	=
>>=	<<=

3.11 Functions

You may use function prototypes to declare a function. These declarations include information about the function parameters. Example:

int alfa(char par1, int par2, long par3);

The actual function definition may be written somewhere else as:

```
int alfa(char par1, int par2, long par3) {
   /* Write some statements here */
```

}

The old Kernighan & Ritchie style of writing function definitions is not supported. Function parameters are passed through the **Data Stack** Function values are returned in registers R30, R31, R22 and R23 (from LSB to MSB).

3.12 Pointers

Due to the Harvard architecture of the AVR microcontroller, with separate address spaces for data (SRAM), program (FLASH) and EEPROM memory, the compiler implements three types of pointers. Variables placed in SRAM are accessed using normal pointers.

For accessing constants placed in FLASH memory, the **flash** or **const** keywords are used. For accessing variables placed in EEPROM, the **eeprom** keyword is used. Although the pointers may point to different memory areas, they are always stored in SRAM. Example:

```
/* Pointer to the string placed in SRAM */
char *ptr_to_ram="This string is placed in SRAM";
/* Pointer to the string placed in FLASH */
char flash *ptr_to_flash="This string is placed in FLASH";
/* Pointer to the string placed in EEPROM */
char eeprom *ptr to eeprom="This string is placed in EEPROM";
```

For improving the code efficiency two memory models are implemented. The **TINY** memory model uses 8 bits for storing pointers to the variables placed in SRAM. In this memory model you can only have access to the first 256 bytes of SRAM. The **SMALL** memory model uses 16 bits for storing pointers the variables placed in SRAM. In this memory model you can have access to 65536 bytes of SRAM. For improving program speed and size, you must always try to use the **TINY** memory model. Pointers to the FLASH and EEPROM memory areas always use 16 bits.

Pointers can be grouped in arrays, which can have up to 8 dimensions. Example:

```
/* Declare and initialize a global array of pointers to strings
  placed in SRAM */
char *strings[3]={"One", "Two", "Three"};
/* Declare and initialize a global array of pointers to strings
  placed in FLASH
   You must note that although the strings are located in FLASH, the
   pointer array itself is located in SRAM */
char flash *messages[3]={"Message 1","Message 2","Message 3"};
/* Declare some strings in EEPROM */
eeprom char m1[]="aaaa";
eeprom char m2[]="bbbb";
void main(void) {
/* Declare a local array of pointers to the strings placed in EEPROM
   You must note that although the strings are located in EEPROM, the
   pointer array itself is located in SRAM */
char eeprom *pp[2];
/* and initialize the array */
pp[0]=m1;
pp[1]=m2;
```

Pointers to functions are always 16 bits wide because they are used to access the FLASH memory area. There is no need to use the **flash** or **const** keywords for these types of pointers. Example:

```
/* Declare a function */
int sum(int a, int b) {
return a+b;
}
/* Declare and initialize a global pointer to the function sum */
int (*sum_ptr) (int a, int b)=sum;
void main(void) {
int i;
/* Call the function sum using the pointer */
i=(*sum_ptr) (1,2);
}
```

3.13 Accessing the I/O Registers

The compiler uses the **sfrb** and **sfrw** keywords to access the AVR microcontroller's I/O Registers, using the IN and OUT assembly instructions. Example:

```
/* Define the SFRs */
sfrb PINA=0x19; /* 8 bit access to the SFR */
sfrw TCNT1=0x2c; /* 16 bit access to the SFR */
void main(void) {
unsigned char a;
a=PINA; /* Read PORTA input pins */
TCNT1=0x1111; /* Write to TCNT1L & TCNT1H registers */
}
```

The addresses of I/O registers are predefined in the following header files, located in the ..\INC subdirectory:

tiny22.h 90s2313.h 90s2323.h 90s2333.h 90s2343.h 90s4414.h 90s4433.h 90s4434.h 90s8515.h 90s8534.h 90s8535.h mega103.h mega128.h mega16.h mega161.h mega163.h mega32.h mega323.h mega603.h mega8.h 94k.h

You may **#include** the corresponding file, for the processor that you use, at the beginning of your program.

3.13.1 Bit level access to the I/O Registers

The bit level access to the I/O registers is accomplished using bit selectors appended after the name of the I/O register.

Because bit level access to I/O registers is done using the CBI, SBI, SBIC and SBIS instructions, the register address must be in the 0 to 1Fh range for **sfrb** and in the 0 to 1Eh range for **sfrw**. Example:

```
sfrb PORTA=0x1b;
sfrb DDRA=0x18;
sfrb PINA=0x19;
void main(void) {
    /* set bit 0 of Port A as output */
DDRA.0=1;
    /* set bit 1 of Port A as input */
DDRA.1=0;
    /* set bit 0 of Port A output */
PORTA.0=1;
    /* test bit 1 input of Port A */
if (PINA.1) { /* place some code here */ };
    /* ..... */
}
```

To improve the readability of the program you may wish to **#define** symbolic names to the bits in I/O registers:

```
sfrb PINA=0x19;
#define alarm_input PINA.2
void main(void)
{
  /* test bit 2 input of Port A */
  if (alarm_input) { /* place some code here */ };
  /* ..... */
}
```

3.14 Accessing the EEPROM

Accessing the AVR internal EEPROM is accomplished using global variables, preceded by the keyword **eeprom**. Example:

```
/* The value 1 is stored in the EEPROM during chip programming */
eeprom int alfa=1;
eeprom char beta;
eeprom long array1[5];
/* The string is stored in the EEPROM during chip programming */
eeprom char string[]="Hello";
void main(void) {
int i;
/* Pointer to EEPROM */
int eeprom *ptr to eeprom;
/* Write directly the value 0x55 to the EEPROM */
alfa=0x55;
/* or indirectly by using a pointer */
ptr to eeprom=&alfa;
*ptr_to_eeprom=0x55;
/* Read directly the value from the EEPROM */
i=alfa;
/* or indirectly by using a pointer */
i=*ptr to eeprom;
}
```

Pointers to the EEPROM always use 16 bits.

3.15 Using Interrupts

The access to the AVR interrupt system is implemented with the **interrupt** keyword. Example:

```
/* Vector numbers are for the AT90S8515 */
/* Called automatically on external interrupt */
interrupt [2] void external_int0(void) {
/* Place your code here */
}
/* Called automatically on TIMER0 overflow */
interrupt [8] void timer0_overflow(void) {
/* Place your code here */
```

}

Interrupt vector numbers start with 1.

The compiler will automatically save all the used registers when calling the interrupt functions and restore them back on exit.

A RETI assembly instruction is placed at the end of the interrupt function.

Interrupt functions can't return a value nor have parameters.

You must also set the corresponding bits in the peripheral control registers to configure the interrupt system and enable the interrupts.

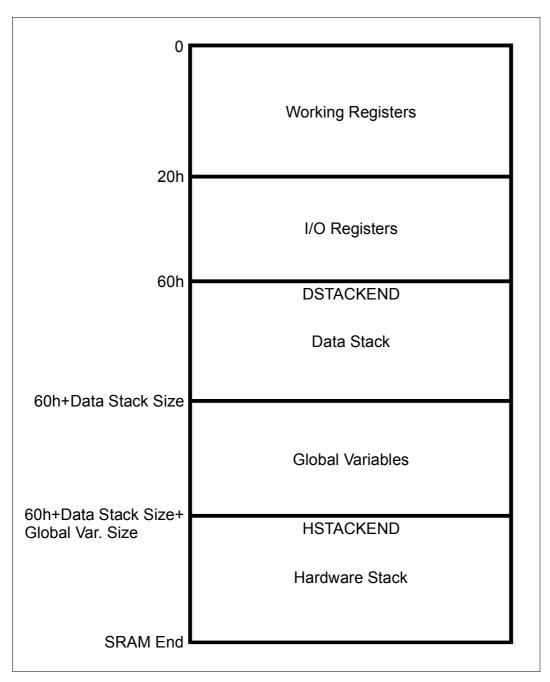
The automatic saving and restoring of registers R0, R1, R22, R23, R24, R25, R26, R27, R30, R31 and SREG, during interrupts can be turned on or off using the **#pragma savereg** directive. Example:

```
/* Turn registers saving off */
#pragma savereg-
/* interrupt handler */
interrupt [1] void my irq(void) {
/* now save only the registers that are
  affected by the routines in the handler,
   for example R30, R31 and SREG */
#asm
   push r30
   push r31
   in r30,SREG
   push r30
#endasm
/* place the C code here */
/* .... */
/* now restore SREG, R31 and R30 */
#asm
   pop r30
   out SREG, r30
   pop r31
   pop r30
#endasm
}
/* re-enable register saving for the other interrupts */
#pragma savereg+
```

The default state is automatic saving of registers during interrupts.

3.16 SRAM Memory Organization

A compiled program has the following memory map:



The Working Registers area contains 32x8 bit general purpose working registers.

The compiler uses the following registers: R0, R1, R22, R23, R24, R25, R26, R27, R28, R29, R30 and R31.

Also some of the registers from R2 to R15 may be allocated by the compiler for global bit variables. The rest of unused registers, in this range, are allocated for global char and int variables. Registers R16 to R21 are allocated for local char and int variables.

The **I/O Registers** area contains 64 addresses for the CPU peripheral functions as Port Control Registers, Timer/Counters and other I/O functions. You may freely use these registers in your assembly programs.

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The **Data Stack** area is used to dynamically store local variables, passing function parameters and saving registers R0, R1, R22, R23, R24, R25, R26, R27, R30, R31 and SREG during interrupt routine servicing.

The Data Stack Pointer is implemented using the Y register.

At start-up the Data Stack Pointer is initialized with the value 5Fh+Data Stack Size.

When saving a value in the Data Stack, the Data Stack Pointer decrements.

When the value is retrieved, the Data Stack Pointer in incremented back.

When configuring the compiler, in the **Project|Configure|C Compiler** menu, you must specify a sufficient **Data Stack Size**, so it will not overlap the **I/O Register** area during program execution.

The **Global Variables** area is used to statically store the global variables during program execution. The size of this area can be computed by summing the size of all the declared global variables.

The **Hardware Stack** area is used for storing the functions return addresses. The SP register is used as a stack pointer and is initialized at start-up with value of last SRAM address.

During the program execution the Hardware Stack grows downwards to the Global Variables area.

When configuring the compiler you have the option to place the strings **DSTACKEND**, respectively **HSTACKEND**, at the end of the **Data Stack**, respectively **Hardware Stack** areas.

When you debug the program with AVR Studio you may see if these strings are overwritten, and consequently modify the **Data Stack Size** using the **Project|Configure|C Compiler** menu command. When your program runs correctly, you may disable the placement of the strings in order to reduce code size.

3.17 Using an External Startup File

In every program the CodeVisionAVR C compiler automatically generates a code sequence to make the following initializations immediately after the AVR chip reset:

- 1. interrupt vector jump table
- 2. global interrupt disable
- 3. EEPROM access disable
- 4. Watchdog Timer disable
- 5. external SRAM access and wait state enable if necessary
- 6. clear registers R2 ... R15
- 7. clear the SRAM
- 8. initialize the global variables located in SRAM
- 9. initialize the Data Stack Pointer register Y
- 10. initialize the Stack Pointer register SP
- 11. initialize the UBRR register if necessary

The automatic generation of code sequences 2 to 8 can be disabled by checking the **Compilation|Use an External Startup Initialization File** check box in the **Project|Configure|C Compiler** dialog window. The C compiler will then include, in the generated .asm file, the code sequences from an external file that must be named **STARTUP.ASM**. This file must be located in the directory where your main C source file resides.

You can write your own **STARTUP.ASM** file to customize or add some features to your program. The code sequences from this file will be immediately executed after the chip reset.

A basic **STARTUP.ASM** file is supplied with the compiler distribution and is located in the ...\BIN directory.

Here's the content of this file:

;CodeVisionAVR C Compiler ;(C) 1998-2001 Pavel Haiduc, HP InfoTech S.R.L. ;EXAMPLE STARTUP FILE .EQU __CLEAR_START=0X60 ;START ADDRESS OF SRAM AREA TO CLEAR .EQU __CLEAR_SIZE=256 ;SIZE OF SRAM AREA TO CLEAR IN BYTES CLI ;DISABLE INTERRUPTS CLR R30 OUT EECR,R30 ;DISABLE EEPROM ACCESS ;DISABLE WATCHDOG LDI R31,0x18 OUT WDTCR,R31 LDI R31,0x10 OUT WDTCR,R31

OUT MCUCR,R30 ;MCUCR=0, NO EXTERNAL SRAM ACCESS ;CLEAR R2-R15 LDI R24,14 LDI R26,2 CLR R27 ____CLEAR_REG:

ST X+,R30 DEC R24 BRNE CLEAR REG

```
;CLEAR SRAM
     LDI R24,LOW( CLEAR SIZE)
      LDI R25, HIGH ( CLEAR SIZE)
      LDI R26, __CLEAR_START
 CLEAR SRAM:
     ST X+,R30
      SBIW R24,1
      BRNE CLEAR SRAM
;GLOBAL VARIABLES INITIALIZATION
      LDI R30,LOW( GLOBAL INI TBL*2)
      LDI R31, HIGH ( GLOBAL INI TBL*2)
 GLOBAL INI NEXT:
     LPM
     MOV R1,R0
      ADIW R30,1
      LPM
     ADIW R30,1
     MOV R22,R30
MOV R23,R31
MOV R31,R0
     MOV R30,R1
      SBIW R30,0
     BREQ __GLOBAL_INI_END
     LPM
     MOV R26,R0
     ADIW R30,1
     LPM
     MOV R27,R0
     ADIW R30,1
     LPM
     MOV R24,R0
     ADIW R30,1
     LPM
     MOV R25,R0
     ADIW R30,1
 GLOBAL INI LOOP:
     LPM
     ST
         X+,R0
     ADIW R30,1
      SBIW R24,1
     BRNE GLOBAL INI LOOP
     MOV R30, R22
     MOV R31, R23
     RJMP GLOBAL INI NEXT
GLOBAL INI END:
```

The **__CLEAR_START** and **__CLEAR_SIZE** constants can be changed to specify which area of SRAM to clear at program initialization.

The **__GLOBAL_INI_TBL** label must be located at the start of a table containing the information necessary to initialize the global variables located in SRAM. This table is automatically generated by the compiler.

3.18 Including Assembly Language in Your Program

You can include assembly language anywhere in your program using the **#asm** and **#endasm** directives. Example:

```
void delay(unsigned char i) {
while (i--) {
    /* Assembly language code sequence */
    #asm
        nop
        nop
        #endasm
    };
}
```

Inline assembly may also be used. Example:

#asm("sei") /* enable interrupts */

The registers R0, R1, R22, R23, R24, R25, R26, R27, R30 and R31 can be freely used in assembly routines.

However when using them in an interrupt service routine the programmer must save, respectively restore, them on entry, respectively on exit, of this routine.

3.18.1 Calling Assembly Functions from C

The following example shows how to access functions written in assembly language from a C program:

```
// function in assembler declaration
// this function will return a+b+c
#pragma warn- // this will prevent warnings
int sum abc(int a, int b, unsigned char c) {
#asm
         r30,v+3 ;R30=LSB a
    ldd
    ldd r31, y+4 ;R31=MSB a
    ldd r26,v+1 ;R26=LSB b
   ldd r27,y+2 ;R27=MSB b
   add r30, r26 ; (R31, R30) = a+b
   adc r31,r27
   ld
        r26,y ;R26=c
   clr r27
                 ;promote unsigned char c to int
   add r30,r26 ; (R31,R30) = (R31,R30) + c
    adc
         r31,r27
#endasm
ļ
#pragma warn+ // enable warnings
void main(void) {
int r;
// now we call the function and store the result in r
r=sum abc(2,4,6);
}
```

The compiler passes function parameters using the Data Stack.

First it pushes the integer parameter a, then b, and finally the unsigned char parameter c. On every push the Y register pair decrements by the size of the parameter (4 for long int, 2 for int, 1 for char).

For multiple byte parameters the MSB is pushed first.

As it is seen the Data Stack grows downward.

After all the functions parameters were pushed on the **Data Stack**, the Y register points to the last parameter c, so the function can read its value in R26 using the instruction: Id r26,y.

The b parameter was pushed before c, so it is at a higher address in the **Data Stack**.

The function will read it using: Idd r27,y+2 (MSB) and Idd r26,y+1 (LSB).

The MSB was pushed first, so it is at a higher address.

The a parameter was pushed before b, so it is at a higher address in the **Data Stack**.

The function will read it using: Idd r31,y+4 (MSB) and Idd r30,y+3 (LSB).

The functions return their values in the registers (from LSB to MSB):

- R30 for char and unsigned char
- R30, R31 for int and unsigned int
- R30, R31, R22, R23 for long and unsigned long.

So our function must return its result in the R30, R31 registers.

After the return from the function the compiler automatically generates code to reclaim the **Data Stack** space used by the function parameters.

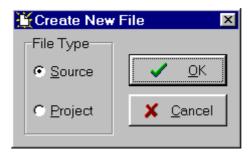
The **#pragma warn-** compiler directive will prevent the compiler from generating a warning that the function does not return a value.

This is needed because the compiler does not know what it is done in the assembler portion of the function.

3.19 Creating Libraries

In order to create your own libraries, the following steps must be followed:

1. Create a header .h file with the prototypes of the library functions. Select the **File**|**New** menu command or press the **New** toolbar button. The following dialog window will open:



Select **Source** and press the **OK** button. A new editor window will be opened for the **untitled.c** source file. Type in the prototype for your function. Example:

<pre>// this #pragma directive will prevent the compiler // from generating a warning that the function was // declared, but not used in the program #pragma used+</pre>
<pre>// library function prototypes int sum(int a, int b); int mul(int a, int b); #pragma used-</pre>
<pre>// this #pragma directive will tell the compiler to // compile/link the functions from the mylib.lib library #pragma library mylib.lib</pre>

Save the file, under a new name, in the ..**INC** directory using the **File|Save As** menu command, for example **mylib.h** :

Save C:\Delph	ii5\AVRC\untitled.c	As		? ×
Save <u>i</u> n: 🔂 I	nc	- [🖻 🛃 🖻 🔳	
 1wire 90c8534 90s2313 90s2323 90s2333 90s2343 90s2343 90s4414 90s4433 	 ■ 90s4434 ■ 90s8515 ■ 90s8535 ■ 94k10 ■ 94k20 ■ 94k40 ■ Bcd ■ Ctype 	 Delay Ds1302 Ds1307 Ds1820 Lcd Lcd4x40 Lcdstk 	i Lm75 i Math i Mega103 i Mega161 i Mega163 i Mega603 i Mem i Pcf8583	S S S S S S S
∢ File <u>n</u> ame:	mylib			
Save as <u>t</u> ype:	C Compiler header fil	e (*.h)	▼ Can	cel

2. Create the library file.

Select the **File**|**New** menu command or press the **New** toolbar button. The following dialog window will open:



Select **Source** and press the **OK** button. A new editor window will be opened for the **untitled.c** source file. Type in the declarations for your functions. Example:

#if	funcused	d sı	ım			
int	sum(int	a,	int	b)	{	
retu	ırn a+b;					
}						
#enc	lif					
#if	funcused	1 mi	ıl			
int	mul(int	a,	int	b)	{	
retu	ırn a*b;					
}						
#end	lif					

The **#if funcused** and **#endif** preprocessor directives allow the functions to be compiled/linked only in case they are effectively used in the program.

Save the file, under a new name, for example **mylib.c** , in any directory using the **File|Save As** menu command:

Save C:\De	phi5\AVRC\untitled.c As	×
Save in: 🧲	🖻 work 💽 🖻 🗾 🛅 🥅 🥅	
		l
		l
		l
		J
File <u>n</u> ame:	mylib Save	
Save as <u>t</u> ype	e: C Compiler source file (*.c) Cancel	

Finally use the **File|Convert to Library** menu command, to save the currently opened file under the name **mylib.lib** in the **..\LIB** directory:

👫 New Library Name			
mylib.lib		-	
✓ <u>о</u> к	🗙 <u>C</u> ancel		

In order to use the newly created **mylib.lib** library, just **#include** the **mylib.h** header file in the beginning of your program. Example:

#include <mylib.h>

All library files must reside in the ...\LIB directory.

3.20 Using the AVR Studio Debugger

CodeVisionAVR is designed to work in conjunction with Atmel AVR Studio debugger version 3.50 or later.

In order to be able to do C source level debugging using AVR Studio, you must select the COFF output file format in the **Project|Configure|Assembler** menu option.

The AVR Studio Debugger is invoked using the **Tools|Debugger** menu command or the **Debugger** toolbar button.

After AVR Studio is executed, the user must select **File|Open** in order to load the COFF file to be debugged.

Once the program is loaded, it can be launched in execution using the **Debug|Go** menu command, by pressing the **F5** key or by pressing the **Execute program** toolbar button. Program execution can be stopped at any time using the **Debug|Break** menu command, by pressing **Ctrl+F5** keys or by pressing the **Break** toolbar button.

To single step the program, the **Debug|Trace Into** (F11 key), **Debug|Step** (F10 key), **Debug|Step Out** menu commands or the corresponding toolbar buttons should be used.

In order to stop the program execution at a specific source line, the **Breakpoints**|**Toggle Breakpoint** menu command, the **F9** key or the corresponding toolbar button should be used.

In order to watch program variables, the user must select **Watch|Add Watch** menu command or press the **Add Watch** toolbar button, and specify the name of the variable in the **Watch** column.

The AVR chip registers can be viewed using the **View**|**Registers** menu command or by pressing the **Alt+0** keys.

The AVR chip PC, SP, X, Y, Z registers and status flags can be viewed using the **View**|**Processor** menu command or by pressing the **Alt+3** keys.

The contents of the FLASH, SRAM and EEPROM memories can be viewed using the **View**|**New Memory View** menu command or by pressing the **Alt+4** keys.

The I/O registers can be viewed using the **View**|**New IO View** menu command or by pressing the **Alt+5** keys.

In order to use the Terminal I/O window, invoked with the **View|Terminal I/O** menu command, for communication with the simulated AVR chip's UART, the COFF file format must be selected and the **Use the Terminal I/O in AVR Studio** check box must be checked in the **Project|Configure|C Compiler** dialog.

To obtain more information about using AVR Studio please consult it's Help system.

3.21 Hints

In order to decrease code size and improve the execution speed, you must apply the following rules:

- If possible use **unsigned** variables;
- Use the smallest data type possible, i.e. bit and unsigned char;
- The size of the bit variables, allocated to the program in the **Project|Configure|C**

Compiler|**Compilation**|**Bit Variables Size** list box, should be as low as possible, in order to free registers for allocation to other global variables;

- If possible use the TINY memory model;
- Always store constant strings in FLASH by using the flash keyword;
- After finishing debugging your program, compile it again with the **Stack End Markers** option disabled.

3.22 Limitations

This version of the CodeVisionAVR C compiler has the following limitations:

- pointers to pointers are not allowed;
- arrays of structures or unions can have only one dimension;
- because some AVR devices have a small amount of SRAM, in order to keep the size of the Data Stack small, structures or unions can't be passed as function parameters. Pointers to structures or unions must be used for this purpose, leading to very efficient SRAM usage;
- bitfields in structures are not implemented. For bit level storage use bit variables.

4. Library Functions Reference

You must **#include** the appropriate header files for the library functions that you use in your program. Example:

```
/* Header files are included before using the functions */
#include <math.h> // for abs
#include <stdio.h> // for putsf
void main(void) {
    int a,b;
    a=-99;
    /* Here you actually use the functions */
b=abs(a);
putsf("Hello world");
}
```

4.1 Character Type Functions

The prototypes for these functions are placed in the file **ctype.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

unsigned char isalnum(char c)

returns 1 if c is alphanumeric.

unsigned char isalpha(char c)

returns 1 if c is alphabetic.

unsigned char isascii(char c)

returns 1 if c is an ASCII character (0..127).

unsigned char iscntrl(char c)

returns 1 if c is a control character (0..31 or 127).

unsigned char isdigit(char c)

returns 1 if c is a decimal digit.

unsigned char islower(char c)

returns 1 if c is a lower case alphabetic character.

unsigned char isprint(char c)

returns 1 if c is a printable character (32..127).

unsigned char ispunct(char c)

returns 1 if c is a punctuation character (all but control and alphanumeric).

unsigned char isspace(char c)

returns 1 c is a white-space character (space, CR, HT).

unsigned char isupper(char c)

returns 1 if c is an upper-case alphabetic character.

unsigned char isxdigit(char c)

returns 1 if c is a hexadecimal digit.

char toascii(char c)

returns the ASCII equivalent of character c.

unsigned char toint(char c)

interprets c as a hexadecimal digit and returns an usigned char from 0 to 15.

char tolower(char c)

returns the lower case of c if c is an upper case character, else c.

char toupper(char c)

returns the upper case of c if c is a lower case character, else c.

4.2 Standard C Input/Output Functions

The prototypes for these functions are placed in the file **stdio.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

The standard C language I/O functions were adapted to work on embedded microcontrollers with limited resources.

The lowest level Input/Output functions are:

char getchar(void)

returns a character received by the UART, using polling.

void putchar(char c)

transmits the character c using the UART, using polling.

Prior to using these functions you must:

```
initialize the UART's Baud rate
   enable the UART transmitter
   enable the UART receiver.
Example:
#include <90s8515.h>
#include <stdio.h>
/* quartz crystal frequency [Hz] */
#define xtal 400000L
/* Baud rate */
#define baud 9600
void main(void) {
char k;
/* initialize the UART's baud rate */
UBRR=xtal/16/baud-1;
/* initialize the UART control register
   RX & TX enabled, no interrupts, 8 data bits */
UCR=0x18;
while (1) {
        /* receive the character */
        k=getchar();
        /* and echo it back */
        putchar(k);
        };
}
```

You can alternatively initialize the UART's Baud rate using the **Project|Configure|C Compiler** menu. If you intend to use other peripherals for Input/Output, you must modify accordingly the **getchar** and **putchar** functions. The source code for these functions is available in the file **stdio.h**.

All the high level Input/Output functions use getchar and putchar.

void puts(char *str)

outputs, using **putchar**, the null terminated character string **str**, located in SRAM, followed by a new line character.

void putsf(char flash *str)

outputs, using **putchar**, the null terminated character string **str**, located in FLASH, followed by a new line character.

void printf(char flash *fmtstr [, arg1, arg2, ...])

outputs formatted text, using **putchar**, according to the format specifiers in the **fmtstr** string. The format specifier string **fmtstr** is constant and must be located in FLASH memory. The implementation of **printf** is a reduced version of the standard C function.

This was necessary due to the specific needs of an embedded system and because the full implementation would require a large amount of memory space.

The following format specifiers are available:

- %c outputs the next argument as an ASCII character
- %d outputs the next argument as a decimal integer
- %i outputs the next argument as a decimal integer
- %u outputs the next argument as an unsigned decimal integer
- %x outputs the next argument as an unsigned hexadecimal integer using lower case letters
- %X outputs the next argument as an unsigned hexadecimal integer using upper case letters
- %s outputs the next argument as a null terminated character string, located in SRAM
- %% outputs the % character

All numeric values are right aligned and left padded with spaces.

If a **0** character is inserted between the % and **d**, **i**, **u**, **x** or **X** then the number will be left padded with 0's.

If a - character is inserted between the % and d, i, u, x or X then the number will be left aligned. A width specifier between 1 and 9 can be inserted between the % and d, i, u, x or X to specify the minimum width of the displayed number.

The displayed number will be right aligned. Placing a - character before the width specifier will left align the number.

void sprintf(char *str, char flash *fmtstr [, arg1, arg2, ...])

this function is identical to **printf** except that the formatted text is placed in the null terminated character string **str**.

char *gets(char *str, unsigned char len)

inputs, using **getchar**, the character string **str** terminated by the new line character. The new line character will be replaced with 0. The maximum length of the string is **len**. If **len** characters were read without encountering the new line character, then the string is terminated with 0 and the function ends.

The function returns a pointer to str.

signed char scanf(char flash *fmtstr [, arg1 address, arg2 address, ...])

formatted text input, using **getchar**, according to the format specifiers in the **fmtstr** string. The format specifier string **fmtstr** is constant and must be located in FLASH memory. The implementation of **scanf** is a reduced version of the standard C function. This was necessary due to the specific needs of an embedded system and because the full implementation would require a large amount of memory space. The following format specifiers are available:

- %c inputs the next argument as an ASCII character
- %d inputs the next argument as a decimal integer
- %i inputs the next argument as a decimal integer
- %u inputs the next argument as an unsigned decimal integer
- %x inputs the next argument as an unsigned hexadecimal integer
- %s inputs the next argument as a null terminated character string

The function returns the number of successful entries, or -1 on error.

signed char sscanf(char *str, char flash *fmtstr [, arg1 address, arg2 address, ...])

this function is identical to **scanf** except that the formatted text is inputted from the null terminated character string **str**, located in SRAM.

4.3 Standard Library Functions

The prototypes for these functions are placed in the file **stdlib.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

int atoi(char *str)

converts the string str to integer.

long int atol(char *str)

converts the string str to long integer.

void itoa(int n, char *str)

converts the integer n to characters in string str.

void Itoa(long int n, char *str)

converts the long integer n to characters in string str.

void ftoa(float n, unsigned char decimals, char *str)

converts the floating point number n to characters in string str. The number is represented with a specified number of decimals.

void ftoe(float n, unsigned char decimals, char *str)

converts the floating point number n to characters in string str. The number is represented as a mantissa with a specified number of decimals and an integer power of 10 exponent (e.g. 12.35e-5).

float atof(char *str)

converts the characters from string str to floating point.

int rand (void)

generates a pseudo-random number between 0 and 32767.

void srand(int seed)

sets the starting value seed used by the pseudo-random number generator in the rand function.

4.4 Mathematical Functions

The prototypes for these functions are placed in the file **math.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

unsigned char cabs(signed char x)

returns the absolute value of the byte x.

unsigned int abs(int x)

returns the absolute value of the integer x.

unsigned long labs(long int x)

returns the absolute value of the long integer x.

float fabs(float x)

returns the absolute value of the floating point number x.

signed char cmax(signed char a, signed char b)

returns the maximum value of bytes a and b.

int max(int a, int b)

returns the maximum value of integers a and b.

long int lmax(long int a, long int b)

returns the maximum value of long integers a and b.

float fmax(float a, float b)

returns the maximum value of floating point numbers a and b.

signed char cmin(signed char a, signed char b)

returns the minimum value of bytes a and b.

int min(int a, int b)

returns the minimum value of integers a and b.

long int lmin(long int a, long int b)

returns the minimum value of long integers a and b.

float fmin(float a, float b)

returns the minimum value of floating point numbers a and b.

signed char csign(signed char x)

returns -1, 0 or 1 if the byte x is negative, zero or positive.

signed char sign(int x)

returns -1, 0 or 1 if the integer x is negative, zero or positive

signed char lsign(long int x)

returns -1, 0 or 1 if the long integer x is negative, zero or positive.

signed char fsign(float x)

returns -1, 0 or 1 if the floating point number x is negative, zero or positive.

unsigned char isqrt(unsigned int x)

returns the square root of the unsigned integer x.

unsigned int lsqrt(unsigned long x)

returns the square root of the unsigned long integer x.

float sqrt(float x)

returns the square root of the positive floating point number x.

float floor(float x)

returns the smallest integer value of the floating point number x.

float ceil(float x)

returns the largest integer value of the floating point number x.

float fmod(float x, float y)

returns the remainder of x divided by y.

float modf(float x, float *ipart)

splits the floating point number x into integer and fractional components. The fractional part of x is returned as a signed floating point number. The integer part is stored as floating point number at ipart.

float Idexp(float x, int expn)

returns x * 2^{expn}.

float frexp(float x, int *expn)

returns the mantisa and exponent of the floating point number x.

float exp(float x)

returns e^x.

float log(float x)

returns the natural logarithm of the floating point number x.

float log10(float x)

returns the base 10 logarithm of the floating point number x.

float pow(float x, float y)

returns x^y.

float sin(float x)

returns the sine of the floating point number x, where the angle is expressed in radians.

float cos(float x)

returns the cosine of the floating point number x, where the angle is expressed in radians.

float tan(float x)

returns the tangent of the floating point number x, where the angle is expressed in radians.

float sinh(float x)

returns the hyperbolic sine of the floating point number x, where the angle is expressed in radians.

float cosh(float x)

returns the hyperbolic cosine of the floating point number $\boldsymbol{x},$ where the angle is expressed in radians.

float tanh(float x)

returns the hyperbolic tangent of the floating point number x, where the angle is expressed in radians.

float asin(float x)

returns the arc sine of the floating point number x (in the range -PI/2 to PI/2). x must be in the range -1 to 1.

float acos(float x)

returns the arc cosine of the floating point number x (in the range 0 to PI). x must be in the range -1 to 1.

float atan(float x)

returns the arc tangent of the floating point number x (in the range -PI/2 to PI/2).

float atan2(float y, float x)

returns the arc tangent of the floating point numbers y/x (in the range -PI to PI).

4.5 String Functions

The prototypes for these functions are placed in the file **string.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

The string manipulation functions were extended to handle strings located both in SRAM and FLASH memories.

char *strcat(char *str1, char *str2)

concatenate the string str2 to the end of the string str1.

char *strcatf(char *str1, char flash *str2)

concatenate the string str2, located in FLASH, to the end of the string str1.

char *strncat(char *str1, char *str2, unsigned char n)

concatenate maximum n characters of the string str2 to the end of the string str1. Returns a pointer to the string str1.

char *strncatf(char *str1, char flash *str2, unsigned char n)

concatenate maximum n characters of the string str2, located in FLASH, to the end of the string str1.

Returns a pointer to the string str1.

char *strchr(char *str, char c)

returns a pointer to the first occurrence of the character c in the string str, else a NULL pointer.

char *strrchr(char *str, char c)

returns a pointer to the last occurrence of the character c in the string str, else a NULL pointer.

signed char strpos(char *str, char c)

returns the index to first occurrence of the character c in the string str, else -1.

signed char strrpos(char *str, char c)

returns the index to the last occurrence of the character c in the string str, else -1.

signed char strcmp(char *str1, char *str2)

compares the string str1 with the string str2. Returns <0, 0, >0 according to str1<str2, str1=str2, str1>str2.

signed char strcmpf(char *str1, char flash *str2)

compares the string str1, located in SRAM, with the string str2, located in FLASH. Returns <0, 0, >0 according to str1<str2, str1=str2, str1>str2.

signed char strncmp(char *str1, char *str2, unsigned char n)

compares at most n characters of the string str1 with the string str2. Returns <0, 0, >0 according to str1<str2, str1=str2, str1>str2.

signed char strncmpf(char *str1, char flash *str2, unsigned char n)

compares at most n characters of the string str1, located in SRAM, with the string str2, located in FLASH.

Returns <0, 0, >0 according to str1<str2, str1=str2, str1>str2.

char *strcpy(char *dest, char *src)

copies the string src to the string dest.

char *strcpyf(char *dest, char flash *src)

copies the string src, located in FLASH, to the string dest, located in SRAM. Returns a pointer to the string dest.

char *strncpy(char *dest, char *src, unsigned char n)

copies at most n characters from the string src to the string dest (null padding). Returns a pointer to the string dest.

char *strncpyf(char *dest, char flash *src, unsigned char n)

copies at most n characters from the string src, located in FLASH, to the string dest, located in SRAM (null padding). Returns a pointer to the string dest.

unsigned char strspn(char *str, char *set)

returns the index of the first character, from the string str, that doesn't match a character from the string set.

If all characters from set are in str returns the length of str.

unsigned char strspnf(char *str, char flash *set)

returns the index of the first character, from the string str, located in SRAM, that doesn't match a character from the string set, located in FLASH. If all characters from set are in str returns the length of str.

unsigned char strcspn(char *str, char *set)

searches the string str for the first occurrence of a character from the string set. If there is a match returns, the index of the character in str. If there are no matching characters, returns the length of str.

unsigned char strcspnf(char *str, char flash *set)

searches the string str for the first occurrence of a character from the string set, located in FLASH. If there is a match, returns the index of the character in str. If there are no matching characters, returns the length of str.

char *strpbrk(char *str, char *set)

searches the string str for the first occurrence of a char from the string set. If there is a match, returns a pointer to the character in str. If there are no matching characters, returns a NULL pointer.

char *strpbrkf(char *str, char flash *set)

searches the string str, located in SRAM, for the first occurrence of a char from the string set, located in FLASH. If there is a match, returns a pointer to the character in str. If there are no matching characters, returns a NULL pointer.

char *strrpbrk(char *str, char *set)

searches the string str for the last occurrence of a character from the string set. If there is a match, returns a pointer to the character in str. If there are no matching characters, returns a NULL pointer.

char *strrpbrkf(char *str, char flash *set)

searches the string str, located in SRAM, for the last occurrence of a character from the string set, located in FLASH.

If there is a match, returns a pointer to the character in str. If there are no matching characters, returns a NULL pointer.

char *strstr(char *str1, char *str2)

searches the string str1 for the first occurrence of the string str2. If there is a match, returns a pointer to the character in str1 where str2 begins. If there is no match, returns a NULL pointer.

char *strstrf(char *str1, char flash *str2)

searches the string str1, located in SRAM, for the first occurrence of the string str2, located in FLASH.

If there is a match, returns a pointer to the character in str1 where str2 begins. If there is no match, returns a NULL pointer.

char *strtok(char *str1, char flash *str2)

scans the string str1, located in SRAM, for the first token not contained in the string str2, located in FLASH.

The function considers the string str1 as consisting of a sequence of text tokens, separated by spans of one or more characters from the string str2.

The first call to strtok, with the pointer to str1 being different from NULL, returns a pointer to the first character of the first token in str1. Also a NULL character will be written in str1, immediately after the returned token.

Subsequent calls to strtok, with NULL as the first parameter, will work through the string str1 until no more tokens remain. When there are no more tokens, strtok will return a NULL pointer.

unsigned char strlen(char *str)

for the TINY memory model. returns the length of the string str (in the range 0..255).

unsigned int strlen(char *str)

for the SMALL memory model. returns the length of the string str (in the range 0..65535).

unsigned int strlenf(char flash *str)

returns the length of the string str located in FLASH.

void *memcpy(void *dest,void *src, unsigned char n)

for the TINY memory model.

void *memcpy(void *dest,void *src, unsigned int n)

for the SMALL memory model.

Copies n bytes from src to dest. dest must not overlap src, else use **memmove**. Returns a pointer to dest.

void *memcpyf(void *dest,void flash *src, unsigned char n)

for the TINY memory model.

void *memcpyf(void *dest,void flash *src, unsigned int n)

for the SMALL memory model.

Copies n bytes from src, located in FLASH, to dest. Returns a pointer to dest.

void *memccpy(void *dest,void *src, char c, unsigned char n)

for the TINY memory model.

void *memccpy(void *dest,void *src, char c, unsigned int n)

for the SMALL memory model.

Copies at most n bytes from src to dest, until the character c is copied. dest must not overlap src. Returns a NULL pointer if the last copied character was c or a pointer to dest+n+1.

void *memmove(void *dest,void *src, unsigned char n)

for the TINY memory model.

void *memmove(void *dest,void *src, unsigned int n)

for the SMALL memory model.

Copies n bytes from src to dest. dest may overlap src. Returns a pointer to dest.

void *memchr(void *buf, unsigned char c, unsigned char n)

for the TINY memory model.

void *memchr(void *buf, unsigned char c, unsigned int n)

for the SMALL memory model.

Scans n bytes from buf for byte c. Returns a pointer to c if found or a NULL pointer if not found.

signed char memcmp(void *buf1,void *buf2, unsigned char n)

for the TINY memory model.

signed char memcmp(void *buf1,void *buf2, unsigned int n)

for the SMALL memory model.

Compares at most n bytes of buf1 with buf2. Returns <0, 0, >0 according to buf1
suf1=buf2, buf1=buf2, buf1>buf2.

signed char memcmpf(void *buf1,void flash *buf2, unsigned char n)

for the TINY memory model.

signed char memcmpf(void *buf1,void flash *buf2, unsigned int n)

for the SMALL memory model.

Compares at most n bytes of buf1, located in SRAM, with buf2, located in FLASH. Returns <0, 0, >0 according to buf1<buf2, buf1=buf2, buf1>buf2.

void *memset(void *buf, unsigned char c, unsigned char n)

for the TINY memory model.

void *memset(void *buf, unsigned char c, unsigned int n)

for the SMALL memory model.

Sets n bytes from buf with byte c. Returns a pointer to buf.

4.6 BCD Conversion Functions

The prototypes for these functions are placed in the file **bcd.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

unsigned char bcd2bin(unsigned char n)

Converts the number n from BCD representation to it's binary equivalent.

unsigned char bin2bcd(unsigned char n)

Converts the number n from binary representation to it's BCD equivalent. The number n values must be from 0 to 99.

4.7 Gray Code Conversion Functions

The prototypes for these functions are placed in the file **gray.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

```
unsigned char gray2binc(unsigned char n)
unsigned char gray2bin(unsigned int n)
unsigned char gray2binl(unsigned long n)
```

Convert the number n from Gray code representation to it's binary equivalent.

```
unsigned char bin2grayc(unsigned char n)
unsigned char bin2gray(unsigned int n)
unsigned char bin2grayl(unsigned long n)
```

Convert the number n from binary representation to it's Gray code equivalent.

4.8 Memory Access Functions

The prototypes for these functions are placed in the file **mem.h**, located in the ...INC subdirectory. This file must be **#include** -ed before using the functions.

void pokeb(unsigned int addr, unsigned char data)

this function writes the byte data to SRAM at address addr.

void pokew(unsigned int addr, unsigned int data)

this function writes the word data to SRAM at address addr. The LSB is written at address addr and the MSB is written at address addr+1.

unsigned char peekb(unsigned int addr)

this function reads a byte located in SRAM at address addr.

unsigned int peekw (unsigned int addr)

this function reads a word located in SRAM at address addr. The LSB is read from address addr and the MSB is read from address addr+1.

4.9 LCD Functions

4.9.1 LCD Functions for displays with up to 2x40 characters

The LCD Functions are intended for easy interfacing between C programs and alphanumeric LCD modules built with the Hitachi HD44780 chip or equivalent.

The prototypes for these functions are placed in the file **Icd.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

Prior to #include -ing the **Icd.h** file, you must declare which microcontroller port is used for communication with the LCD module.

The following LCD formats are supported in **Icd.h**: 1x8, 2x12, 3x12, 1x16, 2x16, 2x20, 4x20, 2x24 and 2x40 characters.

```
Example:
```

```
/* the LCD module is connected to PORTC */
#asm
    .equ __lcd_port=0x15
#endasm
/* now you can include the LCD Functions */
#include <lcd.h>
```

The LCD module must be connected to the port bits as follows:

[LCD] [AVR Port] RS (pin4) ----- bit 0 RD (pin 5) ----- bit 1 EN (pin 6) ----- bit 2 DB4 (pin 11) -- bit 4 DB5 (pin 12) -- bit 5 DB6 (pin 13) -- bit 6 DB7 (pin 14) -- bit 7

You must also connect the LCD power supply and contrast control voltage, according to the data sheet.

The low level LCD Functions are:

void _lcd_ready(void)

waits until the LCD module is ready to receive data. This function must be called prior to writing data to the LCD with the **_lcd_write_data** function.

void _lcd_write_data(unsigned char data)

writes the byte data to the LCD instruction register. This function may be used for modifying the LCD configuration. Example:

```
/* enables the displaying of the cursor */
_lcd_ready();
_lcd_write_data(0xe);
```

void lcd_write_byte(unsigned char addr, unsigned char data);

writes a byte to the LCD character generator or display RAM.

Example:

```
/* LCD user defined characters
  Chip: AT90S8515
   Memory Model: SMALL
   Data Stack Size: 128 bytes
   Use an 2x16 alphanumeric LCD connected
   to the STK200+ PORTC header as follows:
          [STK200+ PORTC HEADER]
   [LCD]
    1 GND- 9 GND
    2 +5V- 10 VCC
    3 VLC- LCD HEADER Vo
    4 RS - 1 PC0
    5 RD - 2
              PC1
    6 EN - 3
             PC2
   11 D4 - 5
             PC4
   12 D5 - 6 PC5
   13 D6 - 7
              PC6
   14 D7 - 8 PC7 */
/* the LCD is connected to PORTC outputs */
#asm
.equ __lcd_port=0x15 ;PORTC
#endasm
/* include the LCD driver routines */
#include <lcd.h>
typedef unsigned char byte;
/* table for the user defined character
   arrow that points to the top right corner */
flash byte char0[8]={
Ob000000,
0b0001111,
0b000011,
0b0000101,
0b0001001,
Ob0010000,
Ob0100000,
Ob1000000};
/* function used to define user characters */
void define char(byte flash *pc,byte char code)
{
byte i,a;
a=(char code << 3) | 0x40;
for (i=0; i<8; i++) lcd write byte(a++,*pc++);</pre>
}
```

```
void main(void)
{
    /* initialize the LCD for 2 lines & 16 columns */
lcd_init(16);
    /* define user character 0 */
define_char(char0,0);
    /* switch to writing in Display RAM */
lcd_gotoxy(0,0);
lcd_putsf("User char 0:");
    /* display used defined char 0 */
lcd_putchar(0);
while (1); /* loop forever */
}
```

unsigned char lcd_read_byte(unsigned char addr);

reads a byte from the LCD character generator or display RAM.

The high level LCD Functions are:

void lcd_init(unsigned char lcd_columns)

initializes the LCD module, clears the display and sets the printing character position at row 0 and column 0. The numbers of columns of the LCD must be specified (e.g. 16). No cursor is displayed. This is the first function that must be called before using the other high level LCD Functions.

void lcd_clear(void)

clears the LCD and sets the printing character position at row 0 and column 0.

void lcd_gotoxy(unsigned char x, unsigned char y)

sets the current display position at column x and row y. The row and column numbering starts from 0.

void lcd_putchar(char c)

displays the character c at the current display position.

void lcd_puts(char *str)

displays at the current display position the string str, located in SRAM.

void lcd_putsf(char flash *str)

displays at the current display position the string str, located in FLASH.

4.9.2 LCD Functions for displays with 4x40 characters

The LCD Functions are intended for easy interfacing between C programs and alphanumeric LCD modules with 4x40 characters, built with the Hitachi HD44780 chip or equivalent. The prototypes for these functions are placed in the file **Icd4x40.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

Prior to #include -ing the **Icd4x40.h** file, you must declare which microcontroller port is used for communication with the LCD module. Example:

/* the LCD module is connected to PORTC */
#asm
 .equ __lcd_port=0x15
#endasm
/* now you can include the LCD Functions */

#include <lcd4x40.h>

The LCD module must be connected to the port bits as follows:

[LCD] [AVR Port] RS (pin 11) --- bit 0 RD (pin 10) --- bit 1 EN1 (pin 9) ---- bit 2 EN2 (pin 15) -- bit 3 DB4 (pin 4) ---- bit 4 DB5 (pin 3) ---- bit 5 DB6 (pin 2) ---- bit 6 DB7 (pin 1) ---- bit 7

You must also connect the LCD power supply and contrast control voltage, according to the data sheet.

The low level LCD Functions are:

void _lcd_ready(void)

waits until the LCD module is ready to receive data. This function must be called prior to writing data to the LCD with the **_lcd_write_data** function.

void _lcd_write_data(unsigned char data)

writes the byte data to the LCD instruction register. This function may be used for modifying the LCD configuration.

Prior calling the low level functions **_lcd_ready** and **_lcd_write_data**, the global variable **_en1_msk** must be set to **LCD_EN1**, respectively **LCD_EN2**, to select the upper, respectively lower half, LCD controller.

Example:

```
/* enables the displaying of the cursor on the upper half
    of the LCD */
_en1_msk=LCD_EN1;
_lcd_ready();
_lcd_write_data(0xe);
```

void lcd_write_byte(unsigned char addr, unsigned char data);

writes a byte to the LCD character generator or display RAM.

unsigned char lcd_read_byte(unsigned char addr);

reads a byte from the LCD character generator or display RAM.

The high level LCD Functions are:

void lcd_init(void)

initializes the LCD module, clears the display and sets the printing character position at row 0 and column 0. No cursor is displayed.

This is the first function that must be called before using the other high level LCD Functions.

void lcd_clear(void)

clears the LCD and sets the printing character position at row 0 and column 0.

void lcd_gotoxy(unsigned char x, unsigned char y)

sets the current display position at column x and row y. The row and column numbering starts from 0.

void lcd_putchar(char c)

displays the character c at the current display position.

void lcd_puts(char *str)

displays at the current display position the string str, located in SRAM.

void lcd_putsf(char flash *str)

displays at the current display position the string str, located in FLASH.

4.9.3 LCD Functions for displays connected in 8 bit memory mapped mode

These LCD Functions are intended for easy interfacing between C programs and alphanumeric LCD modules built with the Hitachi HD44780 chip or equivalent.

The LCD is connected to the AVR external data and address buses as an 8 bit peripheral. This type of connection is used in the Kanda Systems STK200+ and STK300 development boards. For the LCD connection, please consult the documentation that came with your development board. **These function can be used only with AVR chips that allow using external memory devices: AT90S4414, AT90S8515, ATmega603, ATmega103 and ATmega161.**

The prototypes for these functions are placed in the file **Icdstk.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions. The following LCD formats are supported in **Icdstk.h**: 1x8, 2x12, 3x12, 1x16, 2x16, 2x20, 4x20, 2x24 and 2x40 characters.

The LCD Functions are:

void _lcd_ready(void)

waits until the LCD module is ready to receive data. This function must be called prior to writing data to the LCD with the **_LCD_RS0** and **_LCD_RS1** macros. Example:

```
/* enables the displaying of the cursor */
_lcd_ready();
LCD RS0=0xe;
```

The **_LCD_RS0**, respectively **_LCD_RS1**, macros are used for accessing the LCD Instruction Register with RS=0, respectively RS=1.

void lcd_write_byte(unsigned char addr, unsigned char data);

writes a byte to the LCD character generator or display RAM.

Example:

```
/* LCD user defined characters
   Chip: AT90S8515
   Memory Model: SMALL
   Data Stack Size: 128 bytes
   Use an 2x16 alphanumeric LCD connected
   to the STK200+ LCD connector */
/* include the LCD driver routines */
#include <lcdstk.h>
typedef unsigned char byte;
```

```
/* table for the user defined character
   arrow that points to the top right corner */
flash byte char0[8]={
Ob000000,
Ob0001111,
0b000011,
Ob0000101,
Ob0001001,
Ob0010000,
Ob0100000,
Ob1000000};
/* function used to define user characters */
void define char(byte flash *pc,byte char code)
{
byte i,a;
a=(char code << 3) | 0x40;
for (i=0; i<8; i++) lcd write byte(a++,*pc++);</pre>
}
void main(void)
{
/* initialize the LCD for 2 lines & 16 columns */
lcd init(16);
/* define user character 0 */
define char(char0,0);
/* switch to writing in Display RAM */
lcd gotoxy(0,0);
lcd putsf("User char 0:");
/* display used defined char 0 */
lcd putchar(0);
while (1); /* loop forever */
}
```

unsigned char lcd_read_byte(unsigned char addr);

reads a byte from the LCD character generator or display RAM.

void lcd_init(unsigned char lcd_columns)

initializes the LCD module, clears the display and sets the printing character position at row 0 and column 0. The numbers of columns of the LCD must be specified (e.g. 16). No cursor is displayed. This is the first function that must be called before using the other high level LCD Functions.

void lcd_clear(void)

clears the LCD and sets the printing character position at row 0 and column 0.

void lcd_gotoxy(unsigned char x, unsigned char y)

sets the current display position at column x and row y. The row and column numbering starts from 0.

void lcd_putchar(char c)

displays the character c at the current display position.

void lcd_puts(char *str)

displays at the current display position the string str, located in SRAM.

void lcd_putsf(char flash *str)

displays at the current display position the string str, located in FLASH.

4.10 I²C Bus Functions

The I^2C Functions are intended for easy interfacing between C programs and various peripherals using the Philips I^2C bus.

These functions treat the microcontroller as a bus master and the peripherals as slaves. The prototypes for these functions are placed in the file **i2c.h**, located in the ...\INC subdirectory. This file must be **#include** -ed before using the functions.

Prior to #include -ing the **i2c.h** file, you must declare which microcontroller port and port bits are used for communication through the l^2C bus. Example:

```
/* the I<sup>2</sup>C bus is connected to PORTB */
/* the SDA signal is bit 3 */
/* the SCL signal is bit 4 */
#asm
        .equ __i2c_port=0x18
        .equ __sda_bit=3
        .equ __scl_bit=4
#endasm
/* now you can include the I<sup>2</sup>C Functions */
#include <i2c.h>
```

The I²C Functions are:

void i2c_init(void)

this function initializes the I^2C bus. This is the first function that must be called prior to using the other I^2C Functions.

unsigned char i2c_start(void)

issues a START condition. Returns 1 if bus is free or 0 if the I^2C bus is busy.

void i2c_stop(void)

issues a STOP condition.

unsigned char i2c_read(unsigned char ack)

reads a byte from the bus.

The **ack** parameter specifies if an acknowledgement is to be issued after the byte was read. Set **ack** to 0 for no acknowledgement or 1 for acknowledgement.

unsigned char i2c_write(unsigned char data)

```
writes the byte data to the bus.
Returns 1 if the slave acknowledges or 0 if not.
```

Example how to access an Atmel 24C02 256 byte I²C EEPROM:

```
/* the I^2\mathrm{C} bus is connected to PORTB */
/* the SDA signal is bit 3 */
/* the SCL signal is bit 4 */
#asm
    .equ __i2c_port=0x18
    .equ __sda_bit=3
    .equ scl bit=4
#endasm
/* now you can include the I^2C Functions */
#include <i2c.h>
/* function declaration for delay ms */
#include <delay.h>
#define EEPROM BUS ADDRESS 0xa0
/* read a byte from the EEPROM */
unsigned char eeprom read(unsigned char address) {
unsigned char data;
i2c start();
i2c write (EEPROM BUS ADDRESS);
i2c write(address);
i2c start();
i2c write (EEPROM BUS ADDRESS | 1);
data=i2c read(0);
i2c stop();
return data;
}
/* write a byte to the EEPROM */
void eeprom write(unsigned char address, unsigned char data) {
i2c start();
i2c write (EEPROM BUS ADDRESS);
i2c write(address);
i2c write(data);
i2c stop();
/* 10ms delay to complete the write operation */
delay ms(10);
}
void main(void) {
unsigned char i;
/* initialize the I^2C bus */
i2c_init();
/* write the byte 55h at address AAh */
eeprom write(0xaa,0x55);
/* read the byte from address AAh */
i=eeprom read(0xaa);
while (1); /* loop forever */
}
```

4.10.1 National Semiconductor LM75 Temperature Sensor Functions

These functions are intended for easy interfacing between C programs and the LM75 I^2 C bus temperature sensor.

The prototypes for these functions are placed in the file **Im75.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

The I²C bus functions prototypes are automatically **#include** -ed with the **Im75.h**.

Prior to **#include** -ing the **Im75.h** file, you must declare which microcontroller port and port bits are used for communication with the LM75 through the I²C bus. Example:

```
/* the I<sup>2</sup>C bus is connected to PORTB */
/* the SDA signal is bit 3 */
/* the SCL signal is bit 4 */
#asm
    .equ __i2c_port=0x18
    .equ __sda_bit=3
    .equ __scl_bit=4
#endasm
/* now you can include the LM75 Functions */
#include <lm75.h>
```

The LM75 Functions are:

void Im75_init(unsigned char chip,signed char thyst,signed char tos, unsigned char pol)

this function initializes the LM75 sensor chip. Before calling this function the I²C bus must be initialized by calling the i2c_init function. This is the first function that must be called prior to using the other LM75 Functions. If more then one chip is connected to the I²C bus, then the function must be called for each one, specifying accordingly the function parameter **chip**. Maximum 8 LM75 chips can be connected to the I²C bus, their **chip** address can be from 0 to 7. The LM75 is configured in comparator mode, where it functions like a thermostat. The O.S. output becomes active when the temperature exceeds the **tos** limit, and leaves the active state when the temperature drops below the **thyst** limit. Both **thyst** and **tos** are expressed in °C. **pol** represents the polarity of the LM75 O.S. output in active state. If **pol** is 0, the output is active low and if **pol** is 1, the output is active high. Refer to the LM75 data sheet for more information.

int Im75_temperature_10(unsigned char chip)

this function returns the temperature of the LM75 sensor with the address **chip**. The temperature is in $^{\circ}$ C and is multiplied by 10.

Example how to display the temperature of two LM75 sensors with addresses 0 and 1:

```
/* the LM75 I<sup>2</sup>C bus is connected to PORTB */
/* the SDA signal is bit 3 */
/* the SCL signal is bit 4 */
#asm
        .equ __i2c_port=0x18
        .equ __sda_bit=3
        .equ __scl_bit=4
#endasm
```

```
/* include the LM75 Functions */
#include <lm75.h>
/* the LCD module is connected to PORTC */
#asm
    .equ __lcd port=0x15
#endasm
/* include the LCD Functions */
#include <lcd.h>
/* include the prototype for sprintf */
#include <stdio.h>
/* include the prototype for abs */
#include <math.h>
char display buffer[33];
void main(void) {
int t0,t1;
/* initialize the LCD, 2 rows by 16 columns */
lcd init(16);
/* initialize the I^2C bus */
i2c init();
/* initialize the LM75 sensor with address 0 */
/* thyst=20°C tos=25°C */
lm75_init(0,20,25,0);
/* initialize the LM75 sensor with address 1 */
/* thyst=30°C tos=35°C */
lm75 init(1,30,35,0);
/* temperature display loop */
while (1)
      {
      /* read the temperature of sensor \#0 \times 10^{\circ}C \times /
      t0=lm75 temperature 10(0);
      /* read the temperature of sensor \#1 \times 10^{\circ}C \times /
      t1=lm75 temperature 10(1);
      /* prepare the displayed temperatures */
      /* in the display buffer */
      sprintf(display buffer,
      "t0=%-i.%-u%cC\nt1=%-i.%-u%cC",
      t0/10, abs(t0%10), 0xdf, t1/10, abs(t1%10), 0xdf);
      /* display the temperatures */
      lcd clear();
      lcd puts(display buffer);
      };
}
```

4.10.2 Dallas Semiconductor DS1621 Thermometer/Thermostat Functions

These functions are intended for easy interfacing between C programs and the DS1621 I²C bus thermometer/thermostat.

The prototypes for these functions are placed in the file **ds1621.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

The I²C bus functions prototypes are automatically **#include** -ed with the **ds1621.h**.

Prior to **#include** -ing the **ds1621.h** file, you must declare which microcontroller port and port bits are used for communication with the DS1621 through the I²C bus. Example:

```
/* the I<sup>2</sup>C bus is connected to PORTB */
/* the SDA signal is bit 3 */
/* the SCL signal is bit 4 */
#asm
    .equ __i2c_port=0x18
    .equ __sda_bit=3
    .equ __scl_bit=4
#endasm
/* now you can include the DS1621 Functions */
#include <ds1621.h>
```

The DS1621 Functions are:

void ds1621_init(unsigned char chip,signed char tlow,signed char thigh, unsigned char pol)

this function initializes the DS1621 chip.

Before calling this function the I²C bus must be initialized by calling the **i2c_init** function.

This is the first function that must be called prior to using the other DS1621 Functions.

If more then one chip is connected to the I^2C bus, then the function must be called for each one, specifying accordingly the function parameter **chip**.

Maximum 8 DS1621 chips can be connected to the I^2C bus, their **chip** address can be from 0 to 7. Besides measuring temperature, the DS1621 functions also like a thermostat.

The Tout output becomes active when the temperature exceeds the **thigh** limit, and leaves the active state when the temperature drops below the **tlow** limit.

Both tlow and thigh are expressed in °C.

pol represents the polarity of the DS1621 Tout output in active state.

If **pol** is 0, the output is active low and if **pol** is 1, the output is active high.

Refer to the DS1621 data sheet for more information.

unsigned char ds1621_get_status(unsigned char chip)

this function reads the contents of the configuration/status register of the DS1621 with address **chip**.

Refer to the DS1621 data sheet for more information about this register.

void ds1621_set_status(unsigned char chip, unsigned char data)

this function sets the contents of the configuration/status register of the DS1621 with address chip.

Refer to the DS1621 data sheet for more information about this register.

void ds1621_start(unsigned char chip)

this functions exits the DS1621, with address **chip**, from the power-down mode and starts the temperature measurements and the thermostat.

void ds1621_stop(unsigned char chip)

this functions enters the DS1621, with address **chip**, in power-down mode and stops the temperature measurements and the thermostat.

int ds1621_temperature_10(unsigned char chip)

this function returns the temperature of the DS1621 sensor with the address **chip**. The temperature is in $^{\circ}$ C and is multiplied by 10.

Example how to display the temperature of two DS1621 sensors with addresses 0 and 1:

```
/* the DS1621 \rm I^2C bus is connected to PORTB */
/* the SDA signal is bit 3 */
/* the SCL signal is bit 4 */
#asm
    .equ __i2c_port=0x18
    .equ ___sda_bit=3
    .equ __scl bit=4
#endasm
/* include the DS1621 Functions */
#include <ds1621.h>
/* the LCD module is connected to PORTC */
#asm
    .equ __lcd_port=0x15
#endasm
/* include the LCD Functions */
#include <lcd.h>
/* include the prototype for sprintf */
#include <stdio.h>
/\,\star\, include the prototype for abs \,\star/\,
#include <math.h>
char display buffer[33];
void main(void) {
int t0,t1;
/* initialize the LCD, 2 rows by 16 columns */
lcd init(16);
/* initialize the I^2C bus */
i2c init();
/* initialize the DS1621 sensor with address 0 */
/* tlow=20°C thigh=25°C */
ds1621 init(0,20,25,0);
/* initialize the DS1621 sensor with address 1 */
/* tlow=30°C thigh=35°C */
ds1621 init(1,30,35,0);
```

```
/* temperature display loop */
while (1)
      /* read the temperature of DS1621 \#0 *10°C */
      t0=ds1621_temperature_10(0);
      /* read the temperature of DS1621 #1 *10°C */
      t1=ds1621 temperature 10(1);
      /* prepare the displayed temperatures */
      /* in the display buffer */
      sprintf(display buffer,
      "t0=%-i.%-u%cC\nt1=%-i.%-u%cC",
      t0/10,abs(t0%10),0xdf,t1/10,abs(t1%10),0xdf);
      /* display the temperatures */
      lcd_clear();
      lcd puts(display buffer);
      };
}
```

4.10.3 Philips PCF8563 Real Time Clock Functions

These functions are intended for easy interfacing between C programs and the PCF8563 l²C bus real time clock (RTC).

The prototypes for these functions are placed in the file **pcf8563.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

The I²C bus functions prototypes are automatically **#include** -ed with the **pcf8563.h**.

Prior to **#include** -ing the **pcf8563.h** file, you must declare which microcontroller port and port bits are used for communication with the PCF8563 through the I^2C bus. Example:

```
/* the I<sup>2</sup>C bus is connected to PORTB */
/* the SDA signal is bit 3 */
/* the SCL signal is bit 4 */
#asm
    .equ __i2c_port=0x18
    .equ __sda_bit=3
    .equ __scl_bit=4
#endasm
/* now you can include the PCF8563 Functions */
#include <pcf8563.h>
```

The PCF8563 Functions are:

void rtc_init(unsigned char ctrl2, unsigned char clkout, unsigned char timer_ctrl)

this function initializes the PCF8563 chip. Before calling this function the I^2C bus must be initialized by calling the **i2c_init** function. This is the first function that must be called prior to using the other PCF8563 Functions. Only one PCF8583 chip can be connected to the I^2C bus.

The **ctrl2** parameter specifies the initialization value for the PCF8563 **Control/Status 2** register. The **pcf8563.h** header file defines the following macros which allow the easy setting of the **ctrl2** parameter:

- RTC_TIE_ON sets the Control/Status 2 register bit TIE to 1
- RTC AIE ON sets the Control/Status 2 register bit AIE to 1
- RTC_TP_ON sets the Control/Status 2 register bit TI/TP to 1

These macros can be combined using the | operator in order to set more bits to 1.

The **clkout** parameter specifies the initialization value for the PCF8563 **CLKOUT Frequency** register. The **pcf8563.h** header file defines the following macros which allow the easy setting of the **clkout** parameter:

- RTC_CLKOUT_OFF disables the generation of pulses on the PCF8563 CLKOUT output
- RTC_CLKOUT_1 generates 1Hz pulses on the PCF8563 CLKOUT output
- RTC_CLKOUT_32 generates 32Hz pulses on the PCF8563 CLKOUT output
- RTC_CLKOUT_1024 generates 1024Hz pulses on the PCF8563 CLKOUT output
- RTC_CLKOUT_32768 generates 32768Hz pulses on the PCF8563 CLKOUT output.

The **timer_ctrl** parameter specifies the initialization value for the PCF8563 **Timer Control** register. The **pcf8563.h** header file defines the following macros which allow the easy setting of the **timer_ctrl** parameter:

- RTC_TIMER_OFF disables the PCF8563 Timer countdown
- RTC_TIMER_CLK_1_60 sets the PCF8563 Timer countdown clock frequency to 1/60Hz
- RTC_TIMER_CLK_1 sets the PCF8563 Timer countdown clock frequency to 1Hz
- RTC_TIMER_CLK_64 sets the PCF8563 Timer countdown clock frequency to 64Hz
- **RTC_TIMER_CLK_4096** sets the PCF8563 Timer countdown clock frequency to 4096Hz.

Refer to the PCF8563 data sheet for more information.

unsigned char rtc_read(unsigned char address)

this function reads the byte stored in a PCF8563 register at address.

void rtc_write(unsigned char address, unsigned char data)

this function stores the byte data in the PCF8583 register at address.

unsigned char rtc_get_time(unsigned char *hour, unsigned char *min, unsigned char *sec)

void main(void) {
 unsigned char ok,h,m,s;

/* initialize the I²C bus */
i2c init();

```
/* initialize the RTC,
```

```
Timer interrupt enabled,
Alarm interrupt enabled,
CLKOUT frequency=1Hz
Timer clock frequency=1Hz */
rtc_init(RTC_TIE_ON | RTC_AIE_ON,RTC_CLKOUT_1,RTC_TIMER_CLK_1);
/* read time from the RTC */
ok=rtc_get_time(&h,&m,&s);
/* ...... */
```

void rtc_set_time(unsigned char hour, unsigned char min, unsigned char sec)

this function sets the current time of the RTC . The **hour**, **min** and **sec** parameters represent the values of hour, minutes and seconds.

void rtc_get_date(unsigned char *date, unsigned char *month, unsigned *year)

this function returns the current date measured by the RTC . The ***date**, ***month** and ***year** pointers must point to the variables that must receive the values of day, month and year.

void rtc_set_date(unsigned char date, unsigned char month, unsigned year)

this function sets the current date of the RTC .

void rtc_alarm_off(void)

this function disables the RTC alarm function.

void rtc_alarm_on(void)

this function enables the RTC alarm function.

void rtc_get_alarm(unsigned char *date, unsigned char *hour, unsigned char *min)

this function returns the alarm time and date of the RTC. The ***date**, ***hour** and ***min** pointers must point to the variables that must receive the values of date, hour and minutes.

void rtc_set_alarm(unsigned char date, unsigned char hour, unsigned char min)

this function sets the alarm time and date of the RTC. The **date**, **hour** and **min** parameters represent the values of date, hours and minutes. If **date** is set to 0, then this parameter will be ignored. After calling this function the alarm will be turned off. It must be enabled using the **rtc_alarm_on** function.

void rtc_set_timer(unsigned char val)

this function sets the countdown value of the PCF8563 Timer.

4.10.4 Philips PCF8583 Real Time Clock Functions

These functions are intended for easy interfacing between C programs and the PCF8583 l²C bus real time clock (RTC).

The prototypes for these functions are placed in the file **pcf8583.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

The I²C bus functions prototypes are automatically **#include** -ed with the **pcf8583.h**.

Prior to **#include** -ing the **pcf8583.h** file, you must declare which microcontroller port and port bits are used for communication with the PCF8583 through the I²C bus. Example:

```
/* the I<sup>2</sup>C bus is connected to PORTB */
/* the SDA signal is bit 3 */
/* the SCL signal is bit 4 */
#asm
    .equ __i2c_port=0x18
    .equ __sda_bit=3
    .equ __scl_bit=4
#endasm
/* now you can include the PCF8583 Functions */
#include <pcf8583.h>
```

The PCF8583 Functions are:

void rtc_init(unsigned char chip, unsigned char dated_alarm)

this function initializes the PCF8583 chip. Before calling this function the I^2C bus must be initialized by calling the **i2c_init** function. This is the first function that must be called prior to using the other PCF8583 Functions. If more then one chip is connected to the I^2C bus, then the function must be called for each one, specifying accordingly the function parameter **chip**. Maximum 2 PCF8583 chips can be connected to the I^2C bus, their chip address can be 0 or 1. The **dated_alarm** parameter specifies if the RTC alarm takes in account both the time and date (dated_alarm=1), or only the time (dated_alarm=0). Refer to the PCF8583 data sheet for more information. After calling this function the RTC alarm is disabled.

unsigned char rtc_read(unsigned char chip, unsigned char address)

this function reads the byte stored in the PCF8583 SRAM.

void rtc_write(unsigned char chip, unsigned char address, unsigned char data)

this function stores the byte **data** in the PCF8583 SRAM. When writing to the SRAM the user must take in account that locations at addresses 10h and 11h are used for storing the current year value.

unsigned char rtc_get_status(unsigned char chip)

this function returns the value of the PCF8583 control/status register. By calling this function the global variables **__rtc_status** and **__rtc_alarm** are automatically updated. The **__rtc_status** variable holds the value of the PCF8583 control/status register. The **__rtc_alarm** variable takes the value 1 if an RTC alarm occurred. void rtc_get_time(unsigned char chip, unsigned char *hour, unsigned char *min, unsigned char *sec, unsigned char *hsec)

this function returns the current time measured by the RTC. The ***hour**, ***min**, ***sec** and ***hsec** pointers must point to the variables that must receive the values of hour, minutes, seconds and hundreds of a second. Example:

```
#asm
    .equ __i2c_port=0x18
    .equ _____sda__bit=3
    .equ _____scl bit=4
#endasm
#include <pcf8583.h>
void main(void) {
unsigned char h,m,s,hs;
/* initialize the I^2C bus */
i2c init();
/* initialize the RTC 0,
   no dated alarm */
rtc init(0,0);
/* read time from RTC 0*/
rtc get time(0, &h, &m, &s, &hs);
/* ..... */
}
```

void rtc_set_time(unsigned char chip, unsigned char hour, unsigned char min, unsigned char sec, unsigned char hsec)

this function sets the current time of the RTC. The **hour**, **min**, **sec** and **hsec** parameters represent the values of hour, minutes, seconds and hundreds of a second.

void rtc_get_date(unsigned char chip, unsigned char *date, unsigned char *month, unsigned *year)

this function returns the current date measured by the RTC. The ***date**, ***month** and ***year** pointers must point to the variables that must receive the values of day, month and year.

void rtc_set_date(unsigned char chip, unsigned char date, unsigned char month, unsigned year)

this function sets the current date of the RTC.

void rtc_alarm_off(unsigned char chip)

this function disables the RTC alarm function.

void rtc_alarm_on(unsigned char chip)

this function enables the RTC alarm function.

void rtc_get_alarm_time(unsigned char chip, unsigned char *hour, unsigned char *min, unsigned char *sec, unsigned char *hsec)

this function returns the alarm time of the RTC. The ***hour**, ***min**, ***sec** and ***hsec** pointers must point to the variables that must receive the values of hours, minutes, seconds and hundreds of a second.

void rtc_set_alarm_time(unsigned char chip, unsigned char hour, unsigned char min, unsigned char sec, unsigned char hsec)

this function sets the alarm time of the RTC. The **hour**, **min**, **sec** and **hsec** parameters represent the values of hours, minutes, seconds and hundreds of a second.

void rtc_get_alarm_date(unsigned char chip, unsigned char *date, unsigned char *month)

this function returns the alarm date of the RTC. The ***day** and ***month** pointers must point to the variables that must receive the values of date and month.

void rtc_set_alarm_date(unsigned char chip, unsigned char date, unsigned char month)

this function sets the alarm date of the RTC.

4.10.4 Dallas Semiconductor DS1307 Real Time Clock Functions

These functions are intended for easy interfacing between C programs and the DS1307 I²C bus real time clock (RTC).

The prototypes for these functions are placed in the file **ds1307.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

The I²C bus functions prototypes are automatically **#include** -ed with the **ds1307.h**.

Prior to **#include** -ing the **ds1307.h** file, you must declare which microcontroller port and port bits are used for communication with the DS1307 through the I²C bus. Example:

```
/* the I<sup>2</sup>C bus is connected to PORTB */
/* the SDA signal is bit 3 */
/* the SCL signal is bit 4 */
#asm
    .equ __i2c_port=0x18
    .equ __sda_bit=3
    .equ __scl_bit=4
#endasm
/* now you can include the DS1307 Functions */
#include <ds1307.h>
```

The DS1307 Functions are:

void rtc_init(unsigned char rs, unsigned char sqwe, unsigned char out)

this function initializes the DS1307 chip.

Before calling this function the l^2C bus must be initialized by calling the **i2c_init** function. This is the first function that must be called prior to using the other DS1307 Functions. The **rs** parameter specifies the value of the square wave output frequency on the SQW/OUT pin:

- 0 for 1Hz
- 1 for 4096Hz
- 2 for 8192Hz
- 3 for 32768Hz.

If the **sqwe** parameter is set to 1 then the square wave output on the SQW/OUT pin is enabled. The **out** parameter specifies the logic level on the SQW/OUT pin when the square wave output is disabled (sqwe=0).

Refer to the DS1307 data sheet for more information.

void rtc_get_time(unsigned char *hour, unsigned char *min, unsigned char *sec)

this function returns the current time measured by the RTC. The ***hour**, ***min** and ***sec** pointers must point to the variables that must receive the values of hours, minutes and seconds. Example:

```
/* the I<sup>2</sup>C bus is connected to PORTB */
/* the SDA signal is bit 3 */
/* the SCL signal is bit 4 */
#asm
    .equ __i2c_port=0x18
    .equ __sda_bit=3
    .equ __scl_bit=4
#endasm
```

```
#include <ds1307.h>
void main(void) {
unsigned char h,m,s;
/* initialize the I<sup>2</sup>C bus */
i2c_init();
/* initialize the DS1307 RTC */
rtc_init(0,0,0);
/* read time from the DS1307 RTC */
rtc_get_time(&h,&m,&s);
/* ..... */
}
```

void rtc_set_time(unsigned char hour, unsigned char min, unsigned char sec)

this function sets the current time of the RTC. The **hour**, **min** and **sec** parameters represent the values of hour, minutes and seconds.

void rtc_get_date(unsigned char *date, unsigned char *month, unsigned char *year)

this function returns the current date measured by the RTC. The ***date**, ***month** and ***year** pointers must point to the variables that must receive the values of date, month and year.

void rtc_set_date(unsigned char date, unsigned char month, unsigned char year)

this function sets the current date of the RTC.

4.11 Dallas Semiconductor DS1302 Real Time Clock Functions

These functions are intended for easy interfacing between C programs and the DS1302 real time clock (RTC).

The prototypes for these functions are placed in the file **ds1302.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

Prior to **#include** -ing the **ds1302.h** file, you must declare which microcontroller port and port bits are used for communication with the DS1302. Example:

```
/* the DS1302 is connected to PORTB */
/* the IO signal is bit 3 */
/* the SCLK signal is bit 4 */
/* the RST signal is bit 5 */
#asm
    .equ __ds1302_port=0x18
    .equ __ds1302_io=3
    .equ __ds1302_sclk=4
    .equ __ds1302_rst=5
#endasm
/* now you can include the DS1302 Functions */
#include <ds1302.h>
```

The DS1302 Functions are:

void rtc_init(unsigned char tc_on, unsigned char diodes, unsigned char res)

this function initializes the DS1302 chip.

This is the first function that must be called prior to using the other DS1302 Functions. If the **tc_on** parameter is set to 1 then the DS1302's trickle charge function is enabled. The **diodes** parameter specifies the number of diodes used when the trickle charge function is enabled. This parameter can take the value 1 or 2.

The **res** parameter specifies the value of the trickle charge resistor:

- 0 for no resistor
- 1 for a 2kΩ resistor
- 2 for a 4kΩ resistor
- 3 for a 8kΩ resistor.

Refer to the DS1302 data sheet for more information.

unsigned char ds1302_read(unsigned char addr)

this function reads a byte stored at address addr in the DS1302 registers or SRAM.

void ds1302_write(unsigned char addr, unsigned char data)

this function stores the byte data at address addr in the DS1302 registers or SRAM.

void rtc_get_time(unsigned char *hour, unsigned char *min, unsigned char *sec)

this function returns the current time measured by the RTC. The ***hour**, ***min** and ***sec** pointers must point to the variables that must receive the values of hours, minutes and seconds.

Example:

```
#asm
    .equ __ds1302_port=0x18
.equ __ds1302_io=3
    .equ __ds1302_sclk=4
    .equ __ds1302_rst=5
#endasm
#include <ds1302.h>
void main(void) {
unsigned char h,m,s;
/* initialize the DS1302 RTC:
   use trickle charge,
   with 1 diode and 8K resistor */
rtc init(1,1,3);
/* read time from the DS1302 RTC */
rtc get time(&h,&m,&s);
/* ..... */
}
```

void rtc_set_time(unsigned char hour, unsigned char min, unsigned char sec)

this function sets the current time of the RTC. The **hour**, **min** and **sec** parameters represent the values of hour, minutes and seconds.

void rtc_get_date(unsigned char *date, unsigned char *month, unsigned char *year)

this function returns the current date measured by the RTC. The ***date**, ***month** and ***year** pointers must point to the variables that must receive the values of date, month and year.

void rtc_set_date(unsigned char date, unsigned char month, unsigned char year)

this function sets the current date of the RTC.

4.12 1 Wire Protocol Functions

The 1 Wire Functions are intended for easy interfacing between C programs and various peripherals using the Dallas Semiconductor 1 Wire protocol.

These functions treat the microcontroller as a bus master and the peripherals as slaves.

The prototypes for these functions are placed in the file **1wire.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

Prior to **#include** -ing the **1wire.h** file, you must declare which microcontroller port and port bit is used for communication through the 1 Wire protocol. Example:

```
/* the 1 Wire bus is connected to PORTB */
/* the data signal is bit 2 */
#asm
    .equ __w1_port=0x18
    .equ __w1_bit=2
#endasm
/* now you can include the 1 Wire Functions */
#include <1wire.h>
```

Because the 1 Wire Functions require precision time delays for correct operation, the interrupts must be disabled during their execution.

Also it is very important to specify the correct AVR chip clock frequency in the **Project|Configure|C Compiler** menu.

The 1 Wire Functions are:

unsigned char w1_init(void)

this function initializes the 1 Wire devices on the bus. It returns 1 if there were devices present or 0 if not.

unsigned char w1_read(void)

this function reads a byte from the 1 Wire bus.

unsigned char w1_write(unsigned char data)

this function writes the byte **data** to the 1 Wire bus. It returns 1 if the write process completed normally or 0 if not.

unsigned char w1_search(unsigned char cmd,void *p)

this function returns the number of devices connected to the 1 Wire bus. If no devices were detected then it returns 0. The byte **cmd** represents the Search ROM (F0h), Alarm Search (ECh) for the DS1820/DS1822, or other similar commands, sent to the 1 Wire device.

The pointer **p** points to an area of SRAM where are stored the 8 bytes ROM codes returned by the device. After the eighth byte, the function places a ninth status byte which contains a status bit returned by some 1 Wire devices (e.g. DS2405).

Thus the user must allocate 9 bytes of SRAM for each device present on the 1 Wire bus. If there is more then one device connected to the 1 Wire bus, than the user must first call the **w1_search** function to identify the ROM codes of the devices and to be able to address them at a later stage in the program.

Example:

```
#include <90s8515.h>
/* specify the port and bit used for the 1 Wire bus */
#asm
    .equ __w1_port=0x18 ;PORTB
    .equ __w1_bit=2
#endasm
/* include the 1 Wire bus functions prototypes */
#include <1wire.h>
/* include the printf function prototype */
#include <stdio.h>
/* specify the maximum number of devices connected
   to the 1 Wire bus */
#define MAX DEVICES 8
/* allocate SRAM space for the ROM codes & status bit */
unsigned char rom codes[MAX DEVICES,9];
/* quartz crystal frequency [Hz] */
#define xtal 400000L
/* Baud rate */
#define baud 9600
void main(void) {
unsigned char i, j, devices;
/* initialize the UART's baud rate */
UBRR=xtal/16/baud-1;
/* initialize the UART control register
   TX enabled, no interrupts, 8 data bits */
UCR=8;
/* detect how many DS1820/DS1822 devices
   are connected to the bus and
   store their ROM codes in the rom codes array */
devices=w1 search(0xf0,rom codes);
/* display the ROM codes for each detected device */
printf("%-u DEVICE(S) DETECTED\n\r", devices);
if (devices) {
   for (i=0;i<devices;i++) {</pre>
       printf("DEVICE #%-u ROM CODE IS:", i+1);
       for (j=0;j<8;j++) printf("%-X ",rom codes[i,j]);</pre>
       printf("\n\r");
       };
   };
while (1); /* loop forever */
}
```

unsigned char w1_crc8(void *p, unsigned char n)

this function returns the 8 bit DOW CRC for a block of bytes with the length ${\bf n},$ starting from address ${\bf p}.$

4.12.1 Dallas Semiconductor DS1820/DS1822 Temperature Sensors Functions

These functions are intended for easy interfacing between C programs and the DS1820/DS1822 1 Wire bus temperature sensors.

The prototypes for these functions are placed in the file **ds1820.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions.

The 1 Wire bus functions prototypes are automatically #include -ed with the ds1820.h.

Prior to **#include** -ing the **ds1820.h** file, you must declare which microcontroller port and port bits are used for communication with the DS1820/DS1822 through the 1 Wire bus. Example:

```
/* specify the port and bit used for the 1 Wire bus */
#asm
    .equ __w1_port=0x18 ;PORTB
    .equ __w1_bit=2
#endasm
/* include the DS1820/DS1822 functions prototypes */
#include <ds1820.h>
```

The DS1820/DS1822 functions are:

int ds1820_temperature_10(unsigned char *addr)

this function returns the temperature of the DS1820/DS1822 sensor with the ROM code stored in an array of 8 bytes located at address **addr**.

The temperature is in °C and is multiplied by 10. In case of error the function returns the value -9999. If only one DS1820/DS1822 sensor is used, no ROM code array is necessary and the pointer **addr** must be NULL (0).

If several sensors are used, then the program must first identify the ROM codes for all the sensors. Only after that the **ds1820_temperature_10** function may be used, with the **addr** pointer pointing to the array which holds the ROM code for the needed device. Example:

```
#include <90s8515.h>
/* specify the port and bit used for the 1 Wire bus */
#asm
    .equ __w1_port=0x18 ;PORTB
    .equ __w1_bit=2
#endasm
/* include the DS1820/DS1822 functions prototypes */
#include <ds1820.h>
/* include the printf function prototype */
#include <stdio.h>
/* include the abs function prototype */
#include <math.h>
```

```
/* maximum number of DS1820/DS1822 connected to the bus */
#define MAX DEVICES 8
/* DS1820/DS1822 devices ROM code storage area,
   9 bytes are used for each device
   (see the w1 search function description),
   but only the first 8 bytes contain the ROM code
   and CRC */
unsigned char rom codes[MAX DEVICES,9];
main()
unsigned char i, j, devices;
int temp;
/* initialize the UART's baud rate */
UBRR=xtal/16/baud-1;
/* initialize the UART control register
   TX enabled, no interrupts, 8 data bits */
UCR=8;
/* detect how many DS1820/DS1822 devices
   are connected to the bus and
   store their ROM codes in the rom codes array */
devices=w1 search(0xf0,rom codes);
/* display the number */
printf("%-u DEVICE(S) DETECTED\n\r",devices);
/* if no devices were detected then halt */
if (devices==0) while (1); /* loop forever */
/* measure and display the temperature(s) */
while (1)
      {
      for (i=0;i<devices;)</pre>
          {
          temp=ds1820 temperature 10(&rom codes[i,0]);
          printf("t%-u=%-i.%-u\xf8C\n\r",++i,temp/10,
          abs(temp%10));
          };
      };
}
```

unsigned char ds1820_set_alarm(unsigned char *addr,signed char temp_low, signed char temp_high)

this function sets the low (**temp_low**) and high (**temp_high**) temperature alarms of the DS1820/DS1822. In case of success the function returns the value 1, else it returns 0.

The alarm temperatures are stored in both the DS1820/DS1822's scratchpad SRAM and its EEPROM. The ROM code needed to address the device is stored in an array of 8 bytes located at address **addr**. If only one DS1820/DS1822 sensor is used, no ROM code array is necessary and the pointer **addr** must be NULL (0).

The alarm status for all the DS1820/DS1822 devices on the 1 Wire bus can be determined by calling the **w1_search** function with the Alarm Search (ECh) command. Example:

```
#include <90s8515.h>
/* specify the port and bit used for the 1 Wire bus */
#asm
    .equ __w1_port=0x18 ;PORTB
    .equ __w1_bit=2
#endasm
/* include the DS1820/DS1822 functions prototypes */
#include <ds1820.h>
/* include the printf function prototype */
#include <stdio.h>
/* include the abs function prototype */
#include <math.h>
/* maximum number of DS1820/DS1822 connected to the bus */
#define MAX DEVICES 8
/* DS1820/DS1822 devices ROM code storage area,
   9 bytes are used for each device
   (see the w1 search function description),
   but only the first 8 bytes contain the ROM code
   and CRC */
unsigned char rom codes[MAX DEVICES,9];
/* allocate space for ROM codes of the devices
   which generate an alarm */
unsigned char alarm rom codes [MAX DEVICES, 9];
main()
{
unsigned char i, j, devices;
int temp;
/* initialize the UART's baud rate */
UBRR=xtal/16/baud-1;
/* initialize the UART control register
   TX enabled, no interrupts, 8 data bits */
UCR=8;
/* detect how many DS1820/DS1822 devices
   are connected to the bus and
   store their ROM codes in the rom codes array */
devices=w1 search(0xf0,rom codes);
/* display the number */
printf("%-u DEVICE(S) DETECTED\n\r", devices);
/* if no devices were detected then halt */
if (devices==0) while (1); /* loop forever */
```

```
/* set the temperature alarms for all the devices
   temp low=25°C temp high=35°C */
for (i=0;i<devices;i++)</pre>
    {
    printf("INITIALIZING DEVICE #%-u ", i+1);
    if (ds1820_set_alarm(&rom_codes[i,0],25,35))
       putsf("OK"); else putsf("ERROR");
    };
while (1)
      /\,\star\, measure and display the temperature \,\star/\,
      for (i=0;i<devices;)</pre>
           {
          temp=ds1820 temperature 10(&rom codes[i,0]);
          printf("t%-u=%-i.%-u\xf8C\n\r",++i,temp/10,
          abs(temp%10));
           };
      /* display the number of devices which
         generated an alarm */
      printf("ALARM GENERATED BY %-u DEVICE(S)\n\r",
      w1 search(0xec,alarm rom codes));
      };
}
```

Refer to the DS1820/DS1822 data sheet for more information.

4.12.2 Dallas Semiconductor DS2430 EEPROM Functions

These functions are intended for easy interfacing between C programs and the DS2430 1 Wire bus EEPROM.

The prototypes for these functions are placed in the file **ds2430.h**, located in the ..\INC subdirectory. This file must be **#include**-ed before using the functions.

The 1 Wire bus functions prototypes are automatically #include -ed with the ds2430.h.

Prior to #include -ing the **ds2430.h** file, you must declare which microcontroller port and port bits are used for communication with the DS2430 through the 1 Wire bus. Example:

```
/* specify the port and bit used for the 1 Wire bus */
#asm
    .equ __w1_port=0x18 ;PORTB
    .equ __w1_bit=2
#endasm
/* include the DS2430 functions prototypes */
#include <ds2430.h>
```

The DS2430 functions are:

unsigned char ds2430_read_block(unsigned char *romcode,unsigned char *dest, unsigned char addr,unsigned char size);

this function reads a block of **size** bytes starting from the DS2430 EEPROM memory address **addr** and stores it in the string **dest** located in SRAM. It returns 1 if successful, 0 if not.

The DS2430 device is selected using it's ROM code stored in an array of 8 bytes located at address **romcode**.

unsigned char ds2430_read(unsigned char *romcode,unsigned char addr, unsigned char *data);

this function reads a byte from the DS2430 EEPROM memory address **addr** and stores it in the SRAM memory location pointed by **data**.

It returns 1 if successful, 0 if not.

The DS2430 device is selected using it's ROM code stored in an array of 8 bytes located at address **romcode**.

unsigned char ds2430_write_block(unsigned char *romcode, unsigned char *source,unsigned char addr,unsigned char size);

this function writes a block of **size** bytes, from the string **source**, located in SRAM, in the DS2430 EEPROM starting from memory address **addr**.

It returns 1 if successful, 0 if not.

The DS2430 device is selected using it's ROM code stored in an array of 8 bytes located at address **romcode**.

unsigned char ds2430_write(unsigned char *romcode, unsigned char addr,unsigned char data);

this function writes the byte **data** at DS2430 EEPROM memory address **addr**. It returns 1 if successful, 0 if not.

The DS2430 device is selected using it's ROM code stored in an array of 8 bytes located at address **romcode**.

unsigned char ds2430_read_appreg_block(unsigned char *romcode, unsigned char *dest,unsigned char addr,unsigned char size);

this function reads a block of **size** bytes starting from the DS2430 application register address **addr** and stores it in the string **dest** located in SRAM.

It returns 1 if successful, 0 if not.

The DS2430 device is selected using it's ROM code stored in an array of 8 bytes located at address **romcode**.

unsigned char ds2430_write_appreg_block(unsigned char *romcode, unsigned char *source,unsigned char addr,unsigned char size);

this function reads a block of **size** bytes starting from the DS2430 application register address **addr** and stores it in the string **dest** located in SRAM.

It returns 1 if successful, 0 if not.

The DS2430 device is selected using it's ROM code stored in an array of 8 bytes located at address **romcode**.

If only one DS2430 EEPROM is used, no ROM code array is necessary and the pointer **romcode** must be NULL (0).

If several 1 Wire device are used, then the program must first identify the ROM codes for all the devices. Only after that the DS2430 functions may be used, with the **romcode** pointer pointing to the array which holds the ROM code for the needed device. Example:

/* specify the port and bit used for the 1 Wire bus The DS2430 devices are connected to bit 6 of PORTA of the AT90S8515 as follows: [STK200 PORTA HEADER] [DS2430] 1 GND 9 GND -2 DATA 7 PA6 All the devices must be connected in parallel AN 4.7k PULLUP RESISTOR MUST BE CONNECTED BETWEEN DATA (PA6) AND +5V ! */ #asm .equ __w1_port=0x1b .equ w1 bit=6 #endasm // test the DS2430 functions #include <ds2430.h> #include <90s8515.h> #include <stdio.h> /* DS2430 devices ROM code storage area, 9 bytes are used for each device (see the w1 search function description), but only the first 8 bytes contain the ROM code and CRC $\star/$ #define MAX DEVICES 8 unsigned char rom code[MAX DEVICES,9];

```
char text[]="Hello world!";
char buffer[32];
#define START ADDR 2
main() {
unsigned char i, devices;
// init UART
UCR=8;
UBRR=25; // Baud=9600 @ 4MHz
// detect how many 1 Wire devices are present on the bus
devices=w1 search(0xF0,&rom_code[0,0]);
printf("%-u 1 Wire devices found\n\r",devices);
for (i=0;i<devices;i++)</pre>
   // make sure to select only the DS2430 types
   // 0x14 is the DS2430 family code
   if (rom code[i,0]==DS2430 FAMILY CODE)
      printf("\n\r");
      // write text in each DS2430 at START ADDR
      if (ds2430 write block(&rom code[i,0],
         text,START ADDR,sizeof(text)))
         printf("Data written OK in DS2430 #%-u!\n\r",i+1);
         // display the text written in each \ensuremath{\text{DS2430}}
         if (ds2430 read block(&rom code[i,0],buffer,START ADDR,
            sizeof(text)))
            printf("Data read OK!\n\rDS2430 #%-u text: %s\n\r",
            i+1, buffer);
         else printf("Error reading data from DS2430 #%-u!\n\r",
            i+1);
         }
      else printf("Error writing data to DS2430 #%-u!\n\r",i+1);
      };// stop
while (1);
}
```

Refer to the DS2430 data sheet for more information.

4.12.3 Dallas Semiconductor DS2433 EEPROM Functions

These functions are intended for easy interfacing between C programs and the DS2433 1 Wire bus EEPROM.

The prototypes for these functions are placed in the file **ds2433.h**, located in the ..\INC subdirectory. This file must be **#include**-ed before using the functions.

The 1 Wire bus functions prototypes are automatically #include -ed with the ds2433.h.

Prior to #include -ing the **ds2433.h** file, you must declare which microcontroller port and port bits are used for communication with the DS2433 through the 1 Wire bus. Example:

```
/* specify the port and bit used for the 1 Wire bus */
#asm
    .equ __w1_port=0x18 ;PORTB
    .equ __w1_bit=2
#endasm
/* include the DS2433 functions prototypes */
#include <ds2433.h>
```

The DS2433 functions are:

unsigned char ds2433_read_block(unsigned char *romcode,unsigned char *dest, unsigned int addr,unsigned int size);

this function reads a block of **size** bytes starting from the DS2433 EEPROM memory address **addr** and stores it in the string **dest** located in SRAM. It returns 1 if successful, 0 if not.

The DS2433 device is selected using it's ROM code stored in an array of 8 bytes located at address **romcode**.

unsigned char ds2433_read(unsigned char *romcode,unsigned int addr, unsigned char *data);

this function reads a byte from the DS2433 EEPROM memory address **addr** and stores it in the SRAM memory location pointed by **data**.

It returns 1 if successful, 0 if not.

The DS2433 device is selected using it's ROM code stored in an array of 8 bytes located at address **romcode**.

unsigned char ds2433_write_block(unsigned char *romcode, unsigned char *source,unsigned int addr,unsigned int size);

this function writes a block of **size** bytes, from the string **source**, located in SRAM, in the DS2433 EEPROM starting from memory address **addr**.

It returns 1 if successful, 0 if not.

The DS2433 device is selected using it's ROM code stored in an array of 8 bytes located at address **romcode**.

unsigned char ds2433_write(unsigned char *romcode,unsigned int addr, unsigned char data);

this function writes the byte **data** at DS2433 EEPROM memory address **addr**. It returns 1 if successful, 0 if not.

The DS2433 device is selected using it's ROM code stored in an array of 8 bytes located at address **romcode**.

If only one DS2433 EEPROM is used, no ROM code array is necessary and the pointer **romcode** must be NULL (0).

If several 1 Wire device are used, then the program must first identify the ROM codes for all the devices. Only after that the DS2433 functions may be used, with the **romcode** pointer pointing to the array which holds the ROM code for the needed device. Example:

```
/* specify the port and bit used for the 1 Wire bus
   The DS2433 devices are connected to
   bit 6 of PORTA of the AT90S8515 as follows:
   [DS2433]
                 [STK200 PORTA HEADER]
    1 GND
                  - 9 GND
    2 DATA
                  _
                      7
                         PA6
   All the devices must be connected in parallel
   AN 4.7k PULLUP RESISTOR MUST BE CONNECTED
   BETWEEN DATA (PA6) AND +5V !
*/
#asm
    .equ __w1_port=0x1b
    .equ _ w1 bit=6
#endasm
// test the DS2433 functions
#include <ds2433.h>
#include <90s8515.h>
#include <stdio.h>
/* DS2433 devices ROM code storage area,
   9 bytes are used for each device
   (see the w1 search function description),
   but only the first 8 bytes contain the ROM code
   and CRC */
#define MAX DEVICES 8
unsigned char rom code[MAX DEVICES,9];
char text[]="This is a long text to \
be able to test writing across the \setminus
scratchpad boundary";
char buffer[100];
#define START ADDR 2
main() {
unsigned char i, devices;
// init UART
UCR=8;
UBRR=25; // Baud=9600 @ 4MHz
// detect how many 1 Wire devices are present on the bus
devices=w1 search(0xF0,&rom code[0,0]);
printf("%-u 1 Wire devices found\n\r", devices);
```

```
for (i=0;i<devices;i++)</pre>
   // make sure to select only the DS2433 types
   // 0x23 is the DS2433 family code
   if (rom_code[i,0]==DS2433_FAMILY_CODE)
      printf("\n\r");
      // write text in each DS2433 at START_ADDR
      if (ds2433 write block(&rom code[i,0],
         text,START ADDR,sizeof(text)))
         printf("Data written OK in DS2433 #%-u!\n\r",i+1);
         // display the text written in each DS2433
         if (ds2433 read block(&rom code[i,0],buffer,START ADDR,
            sizeof(text))
            printf("Data read OK!\n\rDS2433 #%-u text: %s\n\r",
            i+1, buffer);
         else printf("Error reading data from DS2433 #%-u!\n\r",i+1);
      else printf("Error writing data to DS2433 #%-u!\n\r",i+1);
      };
// stop
while (1);
}
```

Refer to the DS2433 data sheet for more information.

4.13 SPI Functions

The SPI Functions are intended for easy interfacing between C programs and various peripherals using the SPI bus.

The prototypes for these functions are placed in the file **spi.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions. The SPI functions are:

unsigned char spi(unsigned char data)

this function sends the byte data, simultaneously receiving a byte.

Prior to using the **spi** function, you must configure the SPI Control Register SPCR according to the Atmel Data Sheets. Because the **spi** function uses polling for SPI communication, there is no need to set the SPI Interrupt Enable Bit SPIE.

Example of using the **spi** function for interfacing to an AD7896 ADC:

```
/*
  Digital voltmeter using an
  Analog Devices AD7896 ADC
  connected to an AT90S8515
  using the SPI bus
  Chip: AT90S8515
  Memory Model: SMALL
  Data Stack Size: 128 bytes
  Clock frequency: 4MHz
  AD7896 connections to the AT90S8515
   [AD7896]
            [AT9S8515 DIP40]
   1 Vin
   2 Vref=5V
   3 AGND - 20 GND
   4 SCLK - 8 SCK
   5 SDATA - 7 MISO
   6 DGND - 20 GND
   7 CONVST- 2 PB1
   8 BUSY - 1 PB0
  Use an 2x16 alphanumeric LCD connected
  to PORTC as follows:
  [LCD]
         [AT90S8515 DIP40]
  1 GND- 20 GND
  2 +5V- 40 VCC
  3 VLC
  4 RS - 21 PC0
  5 RD - 22 PC1
  6 EN - 23 PC2
 11 D4 - 25 PC4
 12 D5 - 26 PC5
 13 D6 - 27 PC6
 14 D7 - 28 PC7 */
```

#asm .equ __lcd_port=0x15 #endasm #include <lcd.h> // LCD driver routines #include <spi.h> // SPI driver routine #include <90s8515.h> #include <stdio.h> #include <delay.h> // AD7896 reference voltage [mV] #define VREF 5000L // AD7896 control signals PORTB bit allocation #define ADC BUSY PINB.0 #define NCONVST PORTB.1 // LCD display buffer char lcd buffer[33]; unsigned read adc(void) { unsigned result; // start conversion in mode 1 // (high sampling performance) NCONVST=0; NCONVST=1; // wait for the conversion to complete while (ADC BUSY); // read the MSB using SPI result=(unsigned) spi(0)<<8;</pre> // read the LSB using SPI and combine with MSB result|=spi(0); // calculate the voltage in [mV] result=(unsigned) (((unsigned long) result*VREF)/4096L); // return the measured voltage return result; } void main (void) { // initialize PORTB // PB.0 input from AD7896 BUSY // PB.1 output to AD7896 /CONVST // PB.2 & PB.3 inputs // PB.4 output (SPI /SS pin) // PB.5 input // PB.6 input (SPI MISO) // PB.7 output to AD7896 SCLK DDRB=0x92; // initialize the SPI in master mode // no interrupts, MSB first, clock phase negative // SCK low when idle, clock phase=0 // SCK=fxtal/4 SPCR=0x54; // the AD7896 will work in mode 1 // (high sampling performance) // /CONVST=1, SCLK=0

4.14 Power Management Functions

The Power Management Functions are intended for putting the AVR chip in one of its low power consumption modes.

The prototypes for these functions are placed in the file **sleep.h**, located in the ..\INC subdirectory. This file must be **#include** -ed before using the functions. The Power Management Functions are:

void sleep_enable(void)

this function enables entering the low power consumption modes.

void sleep_disable(void)

this function disables entering the low power consumption modes. It is used to disable accidental entering the low power consumption modes.

void idle(void)

this function puts the AVR chip in the idle mode.

Prior to using this function, the **sleep_enable** function must be invoked to allow entering the low power consumption modes.

In this mode the CPU is stopped, but the Timers/Counters, Watchdog and interrupt system continue operating.

The CPU can wake up from external triggered interrupts as well as internal ones.

void powerdown(void)

this function puts the AVR chip in the powerdown mode.

Prior to using this function, the **sleep_enable** function must be invoked to allow entering the low power consumption modes.

In this mode the external oscillator is stopped.

The AVR can wake up only from an external reset, Watchdog time-out or external level triggered interrupt.

void powersave(void)

this function puts the AVR chip in the powersave mode.

Prior to using this function, the **sleep_enable** function must be invoked to allow entering the low power consumption modes.

This mode is similar to the powerdown mode with some differences, please consult the Atmel Data Sheet for the particular chip that you use.

void standby(void)

this function puts the AVR chip in the standby mode.

Prior to using this function, the **sleep_enable** function must be invoked to allow entering the low power consumption modes.

This mode is similar to the powerdown mode with the exception that the external clock oscillator keeps on running.

Consult the Atmel Data Sheet for the particular chip that you use, in order to see if the standby mode is available for it.

void extended_standby(void)

this function puts the AVR chip in the extended standby mode.

Prior to using this function, the **sleep_enable** function must be invoked to allow entering the low power consumption modes.

This mode is similar to the powersave mode with the exception that the external clock oscillator keeps on running.

Consult the Atmel Data Sheet for the particular chip that you use, in order to see if the standby mode is available for it.

4.15 Delay Functions

These functions are intended for generating delays in C programs.

The prototypes for these functions are placed in the file **delay.h**, located in the ...INC subdirectory. This file must be **#include** -ed before using the functions.

Before calling the functions the interrupts must be disabled, otherwise the delays will be much longer then expected.

Also it is very important to specify the correct AVR chip clock frequency in the **Project|Configure|C Compiler** menu.

The functions are:

void delay_us(unsigned int n)

generates a delay of n μ seconds. n must be a constant expression.

void delay_ms(unsigned int n)

generates a delay of n milliseconds.

This function automatically resets the wtachdog timer every 1ms by generating the wdr instruction.

Example:

```
void main(void) {
    /* disable interrupts */
#asm("cli")
    /* 100µs delay */
    delay_us(100);
    /* ..... */
    /* 10ms delay */
    delay_ms(10);
    /* enable interrupts */
#asm("sei")
    /* ..... */
}
```

5. CodeWizardAVR Automatic Program Generator

The CodeWizardAVR Automatic Program Generator allows you to easily write all the code needed for implementing the following functions:

- External memory access setup
- Chip reset source identification
- Input/Output Port initialization
- External Interrupts initialization
- Timers/Counters initialization
- Watchdog Timer initialization
- UART initialization and interrupt driven buffered serial communication
- Analog Comparator initialization
- ADC initialization
- SPI Interface initialization

• I²C Bus, LM75 Temperature Sensor, DS1621 Thermometer/Thermostat, PCF8563, PCF8583, DS1302 and DS1307 Real Time Clocks initialization

- 1 Wire Bus and DS1820/DS1822 Temperature Sensors initialization
- LCD module initialization.

The Automatic Program Generator is invoked using the **Tools|CodeWizardAVR** menu command or by pressing the **CodeWizardAVR** command bar button.

The **File**|**New** menu command allows creating a new CodeWizardAVR project. This project will be named by default **untitled.cwp**.

The File|Open menu command allows loading an existing CodeWizardAVR project:

Open Proj	ect											? ×
Look <u>i</u> n:	🔁 B	lin		_	_	1	• 🗈		r			
🛋 test												
												- 1
												- 1
												. 1
1										_		-
File <u>n</u> ame:		test									<u>O</u> pen	
Files of typ	e:	CodeW	'izardAV	'R proje	ct files ((*.cwp)		•]	0	Cancel	
Files of typ)e:	CodeW	'izardAV	'R proje	ct files ((*.cwp)]		Cancel	

CodeVisionAVR

The **File|Save** menu command allows saving the currently opened CodeWizardAVR project. The **File|Save As** menu command allows saving the currently opened CodeWizardAVR project under a new name:

Save C:\C	VAVE	R\BIN\u	ntitled.c	wp As							? ×
Sa∨e <u>i</u> n:	🔁 Bi	in	_	_		• 🗈	<u></u>	e ř			
											- 1
											- 1
File <u>n</u> ame:		test								<u>B</u> ave	
Save as ty	/pe:	CodeWi	zardAVR	project f	iles (*.cwp)	-]	C	ancel	

The user must set all the AVR chip configuration options.

After that, by selecting the **File|Generate, Save and Exit** menu option, CodeWizardAVR will generate the main C source and project files, save the CodeWizardAVR project file and return to the CodeVisionAVR IDE.

Eventual pin function conflicts will be prompted to the user, allowing him to correct the errors.

Selecting the **File|Exit** menu option allows you to exit the CodeWizardAVR without generating any program files.

By selecting the **Help** menu option you can see the help topic that corresponds to the current CodeWizardAVR configuration menu.

CodeVisionAVR

In the course of program generation the user will be prompted for the name of the main C file:

Save C Compi	ler Source File	? ×
Save <u>i</u> n: 🔂 E	Examples 💽 🖻 🛅 🥅 🗐	
C_asm Ds1820 Eeprom CKeypad Lcddemo Led Max1241 Spi	Therm75	
File <u>n</u> ame:	test <u>Save</u>	
Save as <u>t</u> ype:	C Compiler files (*.c)	el

and for the name of the project file:

Save C Compil	er Project File		? ×
Save <u>i</u> n: 🔂 E	Examples	- 🖻 💆	1 🕋 🔳
C_asm Ds1820 Eeprom Lcddemo Lcddemo Led Max1241 Spi	🗅 Therm75 🏠 Thermicd		
File <u>n</u> ame:	test		Save
Save as type:	Project files (*.prj)		Cancel

5.1 Setting the AVR Chip Options

By selecting the **Chip** tab of the CodeWizardAVR, you can set the AVR chip options.

CodeWizardAVR - untitled.cwp	×
<u>F</u> ile <u>H</u> elp	1
External IRQ Timers UART	
Analog Comparator ADC SPI 12C	10
1 Wire LCD	
Bit-Banged Project Information	
Chip External SRAM Ports	
Chip: ATmega103 💌	
Clock: 4.000000	
Crystal Oscillator Divider Enabled	
Crystal Oscillator Divider: 2 🏹	
Check <u>R</u> eset Source	

The chip type can be specified using the **Chip** list box.

The chip clock frequency in MHz can be specified using the **Clock** spinedit box.

For the AVR chips that contain a crystal oscillator divider, a supplementary **Crystal Oscillator Divider Enabled** check box is visible.

This check box allows you to enable or disable the crystal oscillator divider.

If the crystal oscillator is enabled, you can specify the division ratio using the **Crystal Oscillator Divider** spinedit box.

For the AVR chips that allow the identification of the reset source, a supplementary **Check Reset Source** check box is visible. If it's checked then the CodeWizardAVR will generate code that allows identification of the conditions that caused the chip reset.

CodeVisionAVR

For the AVR chips that allow self-programming, a supplementary **Program Type** list box is visible. It allows to select the type of the generated code:

- Application
- Boot Loader

🕸 CodeWizardAVR - untitled.cwp 👘 🚺	×
<u>F</u> ile <u>H</u> elp	
UART Analog Comparator ADC SPI	
I2C 1 Wire 2 Wire (I2C)	İ
LCD Bit-Banged Project Information	İ
Chip Ports External IRQ Timers	İ
	1
Chip: ATmega163 💌	
Clock: 4.000000 🔨 MHz	
Check <u>R</u> eset Source	
Program Type:	
Application	
,	
	-

5.2 Setting the External SRAM

For the AVR chips that allow connection of external SRAM, you can specify the size of this memory and wait state insertion by selecting the **External SRAM** tab.

롘	CodeWiz	ardAVR	- untitled	l.cw	/p	×
<u> </u>	Eile <u>H</u> elp					1
	External	IRQ	Timers		UART	
	Analog Co	mparator	ADC	SP	1 12C	1
	1 W	Vire		Ŀ	CD	
	Bit-Ban		· ·		formation	
	Chip	Exte	rnal SRA	И	Ports	
	Exte	ernal SRA	AM size: 3	2k	•	
		Evtornal S	RAM Wai	+ 9+4		
	, ,	External a			ale	
1						

The size of external SRAM can be specified using the External SRAM Size list box.

Additional wait states in accessing the external SRAM can be inserted by checking the **External SRAM Wait State** check box.

The MCUCR register in the startup initialization code is configured automatically according to these settings.

For devices, like the ATmega161, that allow splitting the external SRAM in two pages, the External SRAM configuration window will look like this:

.

🏟 CodeWiz	ard	AVR - u	inti	itled.cwp		×
<u>F</u> ile <u>H</u> elp						
External IP	RQ	Timer	в	UARTO	UART1	
Analog Co	mpa	rator S	PI	12C	1 Wire	10
LCD	Bit	_	-	Project In	formation	
Chip		Externa	al S	RAM	Ports	
Extern 0460	ial Si n - 1 F	RAM pa	ge)00	e: 32k 💌 configuration lh - FFFFh Jpper wait s 2r/w+1 addr	on: Intates:	

The **External SRAM page configuration** list box allows selection of the splitting address for the two external SRAM pages .

The wait states that are inserted during external SRAM access, can be specified for the lower, respectively upper, memory pages using the **Lower wait states**, respectively **Upper wait states** list boxes.

The MCUCR and EMCUCR registers in the startup initialization code are configured automatically according to these settings.

5.3 Setting the Input/Output Ports

By selecting the **Ports** tab of the CodeWizardAVR, you can specify the input/output Ports configuration.

🕸 CodeWizard/	VR - untitled.cwp 🛛 🛛
<u>F</u> ile <u>H</u> elp	
External IRQ	Timers UART
Analog Compa	ator ADC SPI 12C
1 Wire	LCD
Bit-Banged	Project Information
Chip	External SRAM Ports
Port A Dort	B Port D Port E
	irection Pullup/Output Value
Bit	
Bit	1 <u>In</u> <u>T</u> Bit 1
Bit	2 <u>In</u> <u>T</u> Bit 2
Bit	3 In T Bit 3
Bit	4 In T Bit 4
Bit	5 In T Bit 5
Bit	6 In T Bit 6
Bit	7 In T Bit 7

You can chose which port you want to configure by selecting the appropriate **PORT x** tab. By clicking on the corresponding **Data Direction** bit you can set the chip pin to be output (Out) or input (In).

The DDRx register will be initialized according to these settings.

By clicking on the corresponding Pullup/Output Value bit you can set the following options:

• if the pin is an input, it can be tri-stated (T) or have an internal pull-up (P) resistor connected to the positive power supply.

• if the pin is an output, it's value can be initially set to 0 or 1.

The PORTx register will be initialized according to these settings.

5.4 Setting the External Interrupts

By selecting the **External IRQ** tab of the CodeWizardAVR, you can specify the external interrupt configuration.

🏟 CodeWizardAVR	- untitled.cwp	×
<u>F</u> ile <u>H</u> elp		
UART Analog Co	mparator ADC	SPI
12C	1 Wire Li	
Bit-Banged	Project Inform	nation
Chip Ports	External IRQ	Timers
INT <u>0</u> Enabled		
✓ INT <u>1</u> Enabled		
✓ INT <u>2</u> Enabled		
✓ INT <u>3</u> Enabled		
✓ INT <u>4</u> Enabled	Mode: Low Leve	
✓ INT <u>5</u> Enabled	Mode: Low Leve	
✓ INT <u>6</u> Enabled	Mode: Low Leve	
INT <u>7</u> Enabled	Mode: Low Leve	

Checking the appropriate **INTx Enabled** check box enables the corresponding external interrupt. If the AVR chip supports this feature, you can select if the interrupt will be edge or level triggered using the corresponding **Mode** list box.

For each enabled external interrupt the CodeWizardAVR will define an **ext_intx_isr** interrupt service routine, where **x** is the number of the external interrupt.

5.5 Setting the Timers/Counters

By selecting the **Timers** tab of the CodeWizardAVR, you can specify the timers/counters configuration.

A number of **Timer** tabs will be displayed according to the AVR chip type.

🕸 CodeWizardAVR - untitled.cwp 🛛 🛛 🖄
<u>F</u> ile <u>H</u> elp
UART Analog Comparator ADC SPI
I2C 1 Wire LCD
Bit-Banged Project Information
Chip Ports External IRQ Timers
Timer 0 Timer 1 Timer 2 Watchdog
Clock Source: System Clock
Clock Value: Timer 0 Stopped 💌
Mode: Output Compare 💌
Output: Disconnected
🗖 Clear Timer on Compare Match
「 <u>O</u> verflow IRQ 「Compare <u>M</u> atch IRQ
Timer Value: 0 h Compare: 0 h

By selecting the **Timer 0** tab you can have the following options:

- Clock Source specifies the timer/counter 0 clock pulse source
- Clock Value specifies the timer/counter 0 clock frequency
- Mode specifies if the timer/counter 0 functioning mode
- Output specifies the function of the timer/counter 0 output and depends of the functioning mode
- **Clear Timer on Compare Match** specifies if the timer/counter 0 is to be reset in the CPU cycle after a compare match
- **Overflow IRQ** specifies if an interrupt is to be generated on timer/counter 0 overflow
- **Compare Match IRQ** specifies if an interrupt is to be generated on timer/counter 0 compare match
- Timer Value specifies the initial value of timer/counter 0 at startup
- Compare specifies the initial value of timer/counter 0 output compare register.

For some devices in Pulse Width Modulation Mode, there will be present a supplementary check box: **PWM frequency x2**. Checking it allows doubling the pulse frequency on the OC0 pin.

If timer/counter 0 interrupts are used the following interrupt service routines may be defined by the CodeWizardAVR:

- timer0_ovf_isr for timer/counter overflow
- **timer0_comp_isr** for timer/counter output compare match.

You must note that depending of the used AVR chip some of these options may not be present. For more information you must consult the corresponding Atmel data sheet.

CodeWizardAVR - untitled.cwp	×
<u>F</u> ile <u>H</u> elp	
UART Analog Comparator ADC SPI	
I2C 1 Wire LCD	i
Bit-Banged Project Information	İ
Chip Ports External IRQ Timer	s
Timer 0 Timer 1 Timer 2 Watchdog	
Clock Source: System Clock	
Clock Value: Timer 1 Stopped 💌	
Mode: Output Compare	
Out. A: Discon. 💌 Out. B: Discon. 💌	
🗖 🗖 Clear Timer Inp. : 🗖 Noise Cancel 🚔	
Interrupt on: 🔲 Timer 1 Overflow	
Val:0 h Cmp.A:0 h Cmp.B:0 h	

By selecting the **Timer 1** tab you can have the following options:

- Clock Source specifies the timer/counter 1 clock pulse source
- Clock Value specifies the timer/counter 1 clock frequency
- Mode specifies if the timer/counter 1 functioning mode
- Out. A specifies the function of the timer/counter 1 output A and depends of the functioning mode
- Out. B specifies the function of the timer/counter 1 output B and depends of the functioning mode
- Clear Timer specifies if the timer/counter 1 is to be reset in the CPU cycle after a compare match
- Inp. specifies the timer/counter 1 capture trigger edge and if the noise canceler is to be used
- Cmp. A specifies the initial value of timer/counter 1 output compare register A

Interrupt specifies if an interrupt is to be generated on timer/counter 1 overflow, input capture and compare match

- Timer Value specifies the initial value of timer/counter 1 at startup
- Cmp. A specifies the initial value of timer/counter 1 output compare register A
- Cmp. B specifies the initial value of timer/counter 1 output compare register B

For some devices in Pulse Width Modulation Mode, there will be present a supplementary check box: **PWM frequency x2**. Checking it allows doubling the pulse frequency on the OC1A and OC1B pins.

If timer/counter 1 interrupts are used the following interrupt service routines may be defined by the CodeWizardAVR:

• timer1_ovf_isr for timer/counter overflow

• timer1_comp_isr or timer1_compa_isr and timer1_compb_isr for timer/counter output compare match

• timer1_capt_isr for timer/counter input capture

You must note that depending of the used AVR chip some of these options may not be present. For more information you must consult the corresponding Atmel data sheet.

🔅 CodeWizardAVR - untitled.cwp	×
<u>F</u> ile <u>H</u> elp	
UART Analog Comparator ADC	SPI
I2C 1 Wire	LCD
Bit-Banged Project Info	rmation
Chip Ports External IRQ	Timers
Timer 0 Timer 1 Timer 2 Wate	hdog
Clock Source: System Clock	•
Clock Value: Timer 2 Stoppe	ed 💌
Mode: Output Compare	•
Output: Disconnected	•
📕 <u>C</u> lear Timer on Compare Mate	h 📗
□ <u>O</u> verflow IRQ □ Compare <u>N</u>	latch IRQ
Timer Value: 0 h Compare:	0 h

By selecting the **Timer 2** tab you can have the following options:

- Clock Source specifies the timer/counter 2 clock pulse source
- Clock Value specifies the timer/counter 2 clock frequency
- **Mode** specifies if the timer/counter 2 functioning mode
- Output specifies the function of the timer/counter 2 output and depends of the functioning mode
- **Clear Timer on Compare Match** specifies if the timer/counter 2 is to be reset in the CPU cycle after a compare match
- Overflow IRQ specifies if an interrupt is to be generated on timer/counter 2 overflow
- Compare Match IRQ specifies if an interrupt is to be generated on timer/counter 2 compare match
- Timer Value specifies the initial value of timer/counter 2 at startup
- Compare specifies the initial value of timer/counter 2 output compare register.

For some devices in Pulse Width Modulation Mode, there will be present a supplementary check box: **PWM frequency x2**. Checking it allows doubling the pulse frequency on the OC2 pin.

If timer/counter 2 interrupts are used the following interrupt service routines may be defined by the CodeWizardAVR:

- timer2_ovf_isr for timer/counter overflow
- **timer2_comp_isr** for timer/counter output compare match.

You must note that depending of the used AVR chip some of these options may not be present. For more information you must consult the corresponding Atmel data sheet.

CodeWiz File Help	zardAVR ·	-untitled.c	wp		×
UART	Analog Com	nparator A	DC	SPI	
I2C	1	Wire	L	ĊD	j
Bit-Bai	nged	Project	Inform	nation	l
Chip	Ports	External II	RQ	Timer	5
শ্ব	Watchdog	Timer <u>E</u> nat	oled		
0.					
	cillator Pres		10EC		
	OSC/16	C OSC	/200		
0	OSC/32	O OSC	/512		
•	OSC/64	O OSC	/1024		
0	OSC/128	⊙ osc	/2048		

By selecting the **Watchdog** tab you can configure the watchdog timer.

Checking the **Watchdog Timer Enabled** check box activates the watchdog timer. You will have then the possibility to set the watchdog timer's **Oscillator Prescaller**. In case the watchdog timer is enabled, you must include yourself the appropriate code sequences to reset it periodically. Example:

#asm("wdr")

For more information about the watchdog timer you must consult the Atmel data sheet for the chip that you use.

5.6 Setting the UART or USART

By selecting the **UART** tab of the CodeWizardAVR, you can specify the UART configuration.

🕸 CodeWizardAVR - untitled.cwp	×
<u>F</u> ile <u>H</u> elp	
I2C 1 Wire LCD	
Bit-Banged Project Information	1
Chip Ports External IRQ Tim	ers
UART Analog Comparator ADC SP	и <u> </u>
✓ Receiver ✓ RxInterrupt	
Receiver Buffer: 8	
☑ <u>T</u> ransmitter ☑ Tx I <u>n</u> terrupt	
Transmitter Buffer: 8	
UART Baud rate: 9600	
Baud Rate Error: 0.2%	
Communication Parameters:	
8 Data, 1 Stop, No Parity 📃	

Checking the **Receiver** check box activates the UART receiver.

The receiver can function in the following modes:

- polled, the **Rx Interrupt** check box isn't checked
- interrupt driven circular buffer, the **Rx Interrupt** check box is checked.

In the interrupt driven mode you can specify the size of the circular buffer using the **Receiver Buffer** spinedit box.

Checking the **Transmitter** check box activates the UART transmitter.

The transmitter can function in the following modes:

- polled, the **Tx Interrupt** check box isn't checked
- interrupt driven circular buffer, the **Tx Interrupt** check box is checked.

In the interrupt driven mode you can specify the size of the circular buffer using the **Transmitter Buffer** spinedit box.

The communication Baud rate can be specified using the **UART Baud Rate** list box. CodeWizardAVR will automatically set the UBRR according to the Baud rate and AVR chip clock frequency. The Baud rate error for these parameters will be calculated and displayed.

The **Communications Parameters** list box allows you to specify the number of data bits, stop bits and parity used for serial communication.

Ma C	adalili	zardAVR -		itlad a			\sim
	<u>H</u> elp		umu	nieu.c	wh		
	SPI	I2C	11	Wire	2 Wir	e (12C)	
L	.CD	Bit-Bange	ed	Proje	ect Infori	mation	1
C	hip (Ports	Ext	ernal IF	RQ 🛛 T	Timers	i
l	JSART	Analog	Con	nparato	or 🗍 A	ADC -	1
	~	<u>R</u> eceiver		🔽 Rx.	<u>I</u> nterrup	ıt	
	Re	ceiver Buffe	er:	8	^,		
	$\mathbf{\nabla}$	<u>T</u> ransmitter	r	▼ TxI	<u>n</u> terrup	t	
	Transmitter Buffer: 8						
	UA	RT Baud ra	ate:	9600	•	<u> </u>	
	Ba	ud Rate Err	or: 0	.2%			
	Co	mmunicatio	in Pa	aramete	ers:		
	8 Data, 1 Stop, No Parity 💌						
	Mode: Asynchronous						
		, ·					

For devices featuring an USART there will be an additional Mode list box.

It allows you to specify the following communication modes:

- Asynchronous
- Synchronous Master, with the UCSRC register's UCPOL bit set to 0
- Synchronous Master, with the UCSRC register's UCPOL bit set to 1
- Synchronous Slave, with the UCSRC register's UCPOL bit set to 0
- Synchronous Slave, with the UCSRC register's UCPOL bit set to 1

The serial communication is realized using the **Standard Input/Output Functions getchar**, **gets**, **scanf**, **putchar**, **puts** and **printf**.

For interrupt driven serial communication, CodeWizardAVR automatically redefines the basic **getchar** and **putchar** functions.

The receiver buffer is implemented using the global array **rx_buffer**.

The global variable **rx_wr_index** is the **rx_buffer** array index used for writing received characters in the buffer.

The global variable **rx_rd_index** is the **rx_buffer** array index used for reading received characters from the buffer by the **getchar** function.

The global variable **rx_counter** contains the number of characters received in **rx_buffer** and not yet read by the **getchar** function.

If the receiver buffers overflows the rx_buffer_overflow global bit variable will be set.

The transmitter buffer is implemented using the global array **tx_buffer**.

The global variable **tx_wr_index** is the **tx_buffer** array index used for writing in the buffer the characters to be transmitted.

The global variable **tx_rd_index** is the **tx_buffer** array index used for reading from the buffer the characters to be transmitted by the **putchar** function.

The global variable **tx_counter** contains the number of characters from **tx_buffer** not yet transmitted by the interrupt system.

For devices with 2 UARTs there will be two tabs present: **UART0** and **UART1**. The functions of configuration check and list boxes will be the same as described above. The UART0 will use the normal **putchar** and **getchar** functions. In case of interrupt driven buffered communication, UART0 will use the following variables: **rx_buffer0**, **rx_wr_index0**, **rx_rd_index0**, **rx_counter0**, **rx_buffer_overflow0**, **tx_buffer0**, **tx_wr_index0**, **tx_rd_index0**, **tx_counter0**. The UART1 will use the **putchar1** and **getchar1** functions. In case of interrupt driven buffered communication, UART1 will use the following variables: **rx_buffer1**, **rx_wr_index1**, **rx_rd_index1**, **rx_counter1**, **rx_buffer_overflow1**, **tx_buffer1**, **tx_wr_index1**, **tx_rd_index1**, **tx_counter1**. All serial I/O using functions declared in **stdio.h**, will be done using UART0.

5.7 Setting the Analog Comparator

By selecting the **Analog Comparator** tab of the CodeWizardAVR, you can specify the analog comparator configuration.

뵠	Code	WizardAVR	- untitled.cwp	×
Ē	Eile <u>H</u> e	elp		
	I2C	1 Wire	2 Wire	LCD
	Bit-E	Banged	Project Infor	mation
	Chip	Ports	External IRQ	Timers
	UART	Analog Cor	mparator ADC	SPI
	L.	7 Analog Cor	mparator <u>E</u> nable	d
		_	/oltage Referenc	
	, T	Input <u>M</u> ultip	-	
	, í		mparator <u>I</u> nterrup	+
		Analog Cor	mparator Input <u>C</u> a	

Checking the **Analog Comparator Enabled** check box enables the on-chip analog comparator. Checking the **Bandgap Voltage Reference** check box will connect an internal voltage reference to the analog comparator's positive input.

Checking the **Input Multiplexer** check box will connect the ADCs analog multiplexer to the analog comparator's negative input.

If you want to generate interrupts if the analog comparator's output changes state, then you must check the **Analog Comparator Interrupt** check box.

The type of output change that triggers the interrupt can be specified in the **Analog Comparator Interrupt Mode** settings.

If the analog comparator's output is to be used for capturing the state of timer/counter 1 then the **Analog Comparator Input Capture** check box must be checked.

Some of this check boxes may not be present on all the AVR chips.

If the analog comparator interrupt is enabled, the CodeWizardAVR will define the **ana_comp_isr** interrupt service routine.

5.8 Setting the Analog-Digital Converter

Some AVR chips contain an analog-digital converter (ADC). By selecting the **ADC** tab of the CodeWizardAVR, you can specify the ADC configuration.

ģ	Code	WizardAV R	- unti	tled.cwp		×
<u>ه</u>	Eile <u>H</u> e	elp				
	I2C	1 Wire		2 Wire	LCD	
	Bit-f	Banged	P	roject Info	rmation	1
	Chip	Ports		ernal IRQ		
	UART	Analog Co	npara	tor ADC	SPI	
			Enclu		o 9 bito	
		_		ed 🔽 Us	se <u>o</u> pils	
		🔽 ADCI				
		F ADC	<u>N</u> oise	Canceler		
		Volt. Ref. :	AREF	pin	•	
		ADC Clock:	250.00)0 kHz	•	
		Automatica	lly Sca	n Inputs-		
		🔽 <u>E</u> nab	led			
l		First Inpu	ıt:	0 7		
l		Last Inpu	t	1 1		
						_

Checking the **ADC Enabled** check box enables the on-chip ADC.

On some AVR devices only the 8 most semnificative bits of the ADC conversion result can be used. This feature is enabled by checking the **Use 8 bits** check box.

If the ADC has an internal reference voltage source, than it can be selected using the **Volt. Ref.** list box or activated by checking the **ADC Bandgap** check box.

If you want to generate interrupts when the ADC finishes the conversion, then you must check the **ADC Interrupt** check box.

If ADC interrupts are used you have the possibility to enable the following functions:

• by checking the **ADC Noise Canceler** check box, the chip is placed in idle mode during the conversion process, thus reducing the noise induced on the ADC by the chip's digital circuitry

• by checking the **Automatically Scan Inputs Enabled** check box, the CodeWizardAVR will generate code to scan an ADC input domain and put the results in an array. The start, respectively the end, of the domain are specified using the **First Input**, respectively the **Last Input**, spinedit boxes. If the automatic inputs scanning is disabled, then a single analog-digital conversion can be executed using the function:

unsigned int read_adc(unsigned char adc_input)

This function will return the analog-digital conversion result for the input **adc_input**. The input numbering starts from 0.

If interrupts are enabled the above function will use an additional interrupt service routine **adc_isr**. This routine will store the conversion result in the **adc_data** global variable.

If the automatic inputs scanning is enabled, the **adc_isr** service routine will store the conversion results in the **adc_data** global array. The user program must read the conversion results from this array.

5.9 Setting the SPI Interface

By selecting the **SPI** tab of the CodeWizardAVR, you can specify the SPI interface configuration.

🏟 CodeWizardAVR -	untitled.cwp	×
<u>F</u> ile <u>H</u> elp		
I2C 1 Wire	2 Wire LCD	
Bit-Banged	Project Information	
Chip Ports	External IRQ Timer	s
UART Analog Comp	parator ADC SPI	
I SPI <u>E</u> nabled	☐ SPI Interrupt	
📕 Clock Rate 🛛	2 Clock Phase	
-SPI Clock Rate-	• Cycle <u>H</u> alf	
1000.000 kHz	C Cycle <u>S</u> tart	
C 250.000 kHz	-Clock Polarity-	
C 62.500 kHz		
🔿 31.250 kHz	C <u>H</u> igh	
-SPI Type	Data Order	
	MSB First	
⊂ M <u>a</u> ster	C LSB First	

Checking the SPI Enabled check box enables the on-chip SPI interface.

If you want to generate interrupts upon completion of a SPI transfer, then you must check the **SPI Interrupt** check box.

You have the possibility to specify the following parameters:

- SPI Clock Rate used for the serial transfer
- Clock Phase: the position of the SCK strobe signal edge relative to the data bit
- Clock Polarity: low or high in idle state
- SPI Type: the AVR chip is master or slave
- **Data Order** in the serial transfer.

Checking the **Clock Rate x2** check box, available for some AVR chips, will double the **SPI Clock Rate**.

For communicating through the SPI interface, with disabled SPI interrupt, you must use the **SPI Functions**.

If the SPI interrupt is enabled, you must use the **spi_isr** interrupt service routine, declared by the CodeWizardAVR.

5.10 Setting the I²C Bus

By selecting the l^2C tab of the CodeWizardAVR, you can specify the l^2C bus configuration.

🌞 CodeWizardAVR - untitled.cwp	×
<u>F</u> ile <u>H</u> elp	
UART Analog Comparator ADC SPI	
Bit-Banged Project Information	j
Chip Ports External IRQ Time	rs
I2C 1 Wire LCD	Í
I2C Port: PORTA 💌	
SDA Bit: 0 💌 SCL Bit: 1 💌	
LM75 DS1621 PCF8563 P(
F Enabled	

Using the l^2C Port list box you can specify which port is used for the implementation of the l^2C bus. The SDA Bit and SCL Bit list boxes allow you to specify which port bits the l^2C bus uses.

5.10.1 Setting the LM75 devices

If you use the LM75 temperature sensor, you must select the LM75 tab and check the LM75 Enabled check box.

🏶 CodeWizardAV	'R - untitled.cwp	х
<u>F</u> ile <u>H</u> elp		
UART Analog C	Comparator ADC SPI	
Bit-Banged	Project Information	
Chip Ports	External IRQ Timers	
I2C	1 Wire LCD	
	RTA - SCL Bit: 1 - 621 PCF8563 P(
Enabled	Address: 🔽 0 🚍	
🗖 Output Ac	tive High	
Temperatu	re °C	
Hyst. : 75	0.S.: 80 1	

The **LM75** Address list box allows you to specify the 3 lower bits of the I^2C addresses of the LM75 devices connected to the bus. Maximum 8 LM75 devices can be used.

The Output Active High check box specifies the active state of the LM75 O.S. output.

The **Hyst.**, respectively **O.S.**, spinedit boxes specify the hysterezis, respectively O.S. temperatures. The LM75 devices are accessed through the **National Semiconductor LM75 Temperature Sensor Functions**.

5.10.2 Setting the DS1621 devices

If you use the DS1621 thermometer/thermostat, you must select the **DS1621** tab and check the **DS1621 Enabled** check box.

🏟 CodeWizardAVR	- untitled.cwp	×
<u>F</u> ile <u>H</u> elp		
UART Analog Co	mparator 🛛 ADC	SPI
Bit-Banged	Project Info	rmation
Chip Ports	External IRQ	Timers
I2C	1 Wire	LCD
I2C Port: POR	TA 👤	
SDA Bit: 0	SCL Bit: 1	
10 1		_
LM75 DS162	21 PCF8563 F	ञ ∢ । ।
🔽 Enabled	Address: 🔽	0 ∄
🗖 Output Activ	/e High	
Temperature	trigger °C	
Low: 50	🔨 High : 55	7

The **Output Active High** check box specifies the active state of the DS1621 Tout output.

The **Low**, respectively **High**, spinedit boxes specify the low, respectively high temperatures trigger temperatures for the Tout output.

The DS1621 devices are accessed through the **Dallas Semiconductor DS1621 Thermometer/Thermostat** functions.

5.10.3 Setting the PCF8563 devices

If you use the PCF8563 RTC, you must select the PCF8563 tab and check the PCF8563 Enabled check box.

🏶 CodeWizardAVR - untitled.cwp 🛛 🛛 🛛
<u>F</u> ile <u>H</u> elp
UART Analog Comparator ADC SPI
Bit-Banged Project Information
Chip Ports External IRQ Timers
I2C 1 Wire LCD
I2C Port: PORTA 🔻
SDA Bit 0 💌 SCL Bit 1 💌
LM75 DS1621 PCF8563 P(+ +
F Enabled CLKOUT: 1
,
Alarm Interrupt
Clock: 1 🔽 🔽 Int. Enabled
Value: 1 🔀 🔽 INT Pulses

The **CLKOUT** list box specifies the frequency of the pulses on the CLKOUT output.

The **Alarm Interrupt** check box enables the generation of interrupts, on the INT pin, when the alarm conditions are met.

The Timer|Clock list box specifies the countdown frequency of the PCF8563 Timer.

If the **Int. Enabled** check box is checked, an interrupt will be generated when the Timer countdown value will be 0.

If the **INT Pulses** check box is checked, the INT pin will issue short pulses when the Timer countdown value reaches 0.

The **Timer**|**Value** spinedit box specifies the Timer reload value when the countdown reaches 0.

The PCF8563 devices are accessed through the Philips PCF8563 Real Time Clock Functions.

5.10.4 Setting the PCF8583 devices

If you use the PCF8583 RTC, you must select the **PCF8583** tab and check the **PCF8583 Enabled** check box.

🌞 CodeWizardAVR - untitled.cwp	×
<u>F</u> ile <u>H</u> elp	
Bit-Banged Project Information	
Chip Ports External IRQ Time	rs 📔
UART Analog Comparator ADC SPI	
I2C 1 Wire LCD	
I2C Port: PORTA 💌	
SDA Bit: 0 💌	
SCL Bit: 1 💌	
LM75 DS1621 PCF8583 D	
🔽 Enabled Address: 🔽 0 🏯	

The **PCF8583 Address** list box allows you to specify the low bit of the I²C addresses of the PCF8583 devices connected to the bus. Maximum 2 PCF8583 devices can be used.

The PCF8583 devices are accessed through the Philips PCF8583 Real Time Clock Functions.

5.10.5 Setting the DS1307 devices

If you use the DS1307 RTC, you must select the **DS1307** tab and check the **DS1307 Enabled** check box.

🏶 CodeWizardAVR	- untitled.cwp	×	
<u>F</u> ile <u>H</u> elp			
UART Analog Cor	nparator ADC SPI		
Bit-Banged	Project Information	İ.	
Chip Ports	External IRQ Timers		
I2C 1	Wire LCD		
I2C Port: PORT	A 🗾		
SDA Bit: 0	SCL Bit: 1 💌		
PCF8563 PCF8583 DS1307			
	Square Wa∨e Output Γ Enabled		

The DS1307 device is accessed through the **Dallas Semiconductor DS1307 Real Time Clock Functions**.

In case the square wave signal output is disabled, the state of the SQW/OUT pin can be specified using the **OUT** list box.

By checking the **Square Wave Output Enabled** check box a square wave signal will be available on the DS1307's SQW/OUT pin. The frequency of the square wave can be selected using the **Freq.** list box.

CodeWizardAVR - untitled.cwp
File Help
UART Analog Comparator ADC SPI
Bit-Banged Project Information
Chip Ports External IRQ Timers
I2C 1 Wire LCD
I2C Port: PORTA 🔻
,
SDA Bit: 0 💌 SCL Bit: 1 💌
PCF8563 PCF8583 DS1307
-Square Wave Output
Enabled F Enabled
Freq.: 1 💌 Hz

5.11 Setting the 1 Wire Bus

By selecting the **1 Wire** tab of the CodeWizardAVR, you can specify the 1 Wire bus configuration.

🕸 CodeW	/izardAVR	- untitled.cwp	×	
<u>F</u> ile <u>H</u> el	р			
Bit-Bi	anged 🛛	Project Inforr	nation	
Chip	Ports	External IRQ	Timers	
UART	Analog Cor	mparator ADC	SPI	
I2C		1 Wire L	.CD D.	
	1 Wire Por	t PORTB 💌		
	Data Bit:	0		
	-DS1820-			
□ Enabled				

Using the **1 Wire Port** list box you can specify which port is used for the implementation of the 1 Wire bus.

The **Data Bit** list box allows you to specify which port bit the 1 Wire bus uses.

If you use the DS1820/DS1822 temperature sensors, you must check the **DS1820/DS1822 Enabled** check box.

🌼 CodeW	izardAVR	- untitled.cwp	×
<u>F</u> ile <u>H</u> elp	D		
Bit-Ba	anged	Project Inform	nation
Chip	Ports	External IRQ	Timers
-			SPI
I2C		1 Wire L	.CD [
	1 Wire Por	t: PORTB 💌	
	Data Bit:	0	
	-DS1820-		
Multiple Devices			

If you use several DS1820/DS1822 devices connected to the 1 Wire bus, you must check the **Multiple Devices** check box. Maximum 8 DS1820/DS1822 devices can be connected to the bus. The ROM codes for these devices will be stored in the **ds1820_rom_codes** array. The DS1820/DS1822 devices can be accessed using the **Dallas Semiconductor DS1820/DS1822 Temperature Sensors Functions**.

5.12 Setting the 2 Wire Bus

By selecting the **2 Wire (I²C)** tab of the CodeWizardAVR, you can specify the 2 Wire bus interface configuration.

	zardAVR	- untitlec	l.cwp		×
<u>F</u> ile <u>H</u> elp		,			
· · · · ·	Analog Con				ļ
LCD	_	ed Pr	· .		
Chip	·	Externa			
I2C	1 1 1	Vire	2 WI	re (I2C)	
2 ک ا	Wire <u>E</u> nat	oled			
ГC	Generate <u>A</u>	cknowled	lge Puls	e	
Slav	/e Address	s: 0 h			
	General <u>C</u> a	ll Recogn	iition		
Bit F	Rate: 250).000 kHz	•		
Γ 2	: Wire <u>I</u> nterr	rupt			

The AVR chip's 2 Wire interface can be enabled by checking the **2 Wire Enabled** check box. If the **Generate Acknowledge Pulse** check box is checked the ACK pulse on the 2 Wire bus is generated if one of the following conditions is met:

- the device's own slave address has been received;
- a General Call has been received and the General Call Recognition check box is checked;
- a data byte has been received in master receiver or slave receiver mode.

If the **Generate Acknowledge Pulse** check box is not checked, the chip's 2 Wire interface is virtually disconnected from the 2 Wire bus. This check box will set the state of the TWEA bit of the TWCR register.

The **Slave Address** edit box sets the slave address of the 2 Wire serial bus unit. This address must be specified in hexadecimal and will be used to initialize the bits 1..7 of the TWAR register. Checking the **General Call Recognition** check box, enables the recognition of the General Call given

over the 2 Wire bus. This check box will set the state of the TWGCE bit of the TWAR register. The **Bit Rate** list box allows you to specify maximum frequency of the pulses on the SCL 2 Wire bus line. It will affect the value of the TWBR register.

As both the receiver and transmitter may stretch the duration of the low period of the SCL line, when waiting for response, the frequency of the pulses may be lower than specified.

If the **2 Wire Interrupt** check box is checked, the 2 Wire interface will generate interrupts. These interrupts will be serviced by the **twi_isr** function.

5.13 Setting the LCD

By selecting the LCD tab of the CodeWizardAVR, you can specify the LCD configuration.

Eile Help Bit-Banged Project Information Chip Ports External IRQ UART Analog Comparator ADC I2C 1 Wire LCD LCD Port: PORTD ▼ Chars./Line: 16 PORT Bit 0 - RS (LCD Pin 4)
Chip Ports External IRQ Timers UART Analog Comparator ADC SPI I2C 1 Wire LCD LCD Port: PORTD Chars./Line: 16
UART Analog Comparator ADC SPI I2C 1 Wire LCD LCD Port: PORTD Chars./Line: 16
I2C 1 Wire LCD LCD Port: PORTD Chars./Line: 16
LCD Port: PORTD Chars./Line: 16
Chars./Line: 16 💌
Chars./Line: 16 💌
Chars./Line: 16 💌
PORT Bit 0 - RS (LCD Pin 4)
PORT Bit 0 - RS (LCD Pin 4)
PORT Bit 1 - RD (LCD Pin 5)
PORT Bit 2 - EN (LCD Pin 6) POBT Bit 3 - Free
PORT Bit 4 - DB4 (LCD Pin 11)
PORT Bit 5 - DB5 (LCD Pin 12)
PORT Bit 6 - DB6 (LCD Pin 13)
PORT Bit 7 - DB7 (LCD Pin 14)

Using the **LCD Port** list box you can specify which port is used for connecting the alphanumeric LCD. The **Chars./Line** list box allows you to specify the number of characters per display line. This value is used by the **Icd_init** function.

The LCD can be accessed using the standard **LCD Functions**.

5.14 Setting Bit-Banged Peripherals

By selecting the **Bit-Banged** tab of the CodeWizardAVR, you can specify the configuration of the peripherals connected using the bit-banging method.

If you use the DS1302 RTC, you must select the **DS1302** tab.

🔅 CodeWizardAVR - untitled.cwp 🛛 🔹	×
<u>F</u> ile <u>H</u> elp	
Chip Ports External IRQ Timers UART Analog Comparator ADC SPI I2C 1 Wire LCD Bit-Banged Project Information	
DS1302	
Port: PORTE ▼ I/O Bit: 0 ▼ SCLK Bit: 1 ▼ /RST Bit: 2 ▼ Trickle Charge ✓ Enabled Diodes: 1 ▼ Charge Resistor: None ▼	
	1

Using the **Port** list box you can specify which port is used for connecting with the DS1302. The **I/O Bit**, **SCLK Bit** and **/RST Bit** list boxes allow you to specify which port bits are used for this. The DS1302's trickle charge function can be activated by checking the **Trickle Charge|Enabled** check box.

The number of diodes, respectively the charge resistor value, can be specified using the **Trickle Charge|Diodes**, respectively **Trickle Charge|Resistors**, list boxes.

The DS1302 device is accessed through the **Dallas Semiconductor DS1302 Real Time Clock Functions**.

5.15 Specifying the Project Information

By selecting the **Project Information** tab, you can specify the information placed in the comment header, located at the beginning of the C source file produced by CodeWizardAVR.

🏟 CodeWi:	zardAVR	- untitlec	l.cwp		×
<u>F</u> ile <u>H</u> elp					
Chip	Ports	Externa	l IRQ	Timers	
UART	Analog Co	mparator	ADC	SPI	Ĺ
I2C		1 Wire	ĹL	CD	l
Bit-Bar	nged	Proje	ct Inforn	nation	
Project N	ame:				
		_	_		
Version:		Da	ate:		
Author:			_		
Company	<i>r</i> .				
Comment	ts:				

You can specify the **Project Name**, **Date**, **Author**, **Company** and **Comments**.

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The e-mail support addresses are: hpinfotech@xnet.ro and dhptechn@ir.ro .

8. Contact Information

HP InfoTech S.R.L. can be contacted at:

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